


Name: BALU BADHAVATHU											
Qualification : M.TECH Power Electronics JNTUH											
Experience : 10-07-2010 to till Date (8Yrs) He has completed B.TECH in KITSW-2009, and MTECH from VCEW in 2011 and joined in Electrical Department as Lecturer in VCEW.											
Area of Interest: Power Converters and its Applications, PLC, Power Systems and Control Systems											
Subjects Taught: Control Systems, Network Theory, EC-II, Electrical Measurements, Digital Control Systems, Advanced Control Systems, NFS, Power Electronics, PSOC.											
Research Publications: 1. BanothChandulal, B Balu, “Harmonic elimination using a new energy control technique for DVS by simulation,” <i>IOJETR Trans. Power Engg</i> , vol. 18,pp.327-343, August 2015. 2. GANTA SATHWIK ,B.BALU, ” Simulation of back propagation control algorithm for power quality improvement using DSTATCOM” <i>TRANS.POWER.ENGG</i> 2000;vol 18,pp.327-343,oct 2015 3. MD.AKBAR PASHA,B.BALU, ”simulation of soft switching bidirectional isolated full bridge converter with active and passive snubbers.” <i>TRANS.POWER.ENGG</i> 2000;vol 18,pp.327-343,DEC 2015 4. B.SUMAJA,B.BALU “Role of thyrister controlled reactor in power quality improvement” <i>IOJETR Trans. Power Engg</i> , vol. 18,pp.327-343, Feb 2015.											
Grants: NA-											
Projects guided:											
UG projects											
<table border="1"> <thead> <tr> <th data-bbox="180 1370 1090 1408">Title</th> <th data-bbox="1090 1370 1418 1408">Year</th> </tr> </thead> <tbody> <tr> <td data-bbox="180 1408 1090 1585"> 1..A stand –alone PV battery powered pump system designing by using single switch dc/dc converter 2.designing of servo smart stabilizer. 3.modelling and simulation of switched inductor buck boost converter circuit for solar power system. </td> <td data-bbox="1090 1408 1418 1585"> 2017-18 </td> </tr> <tr> <td data-bbox="180 1585 1090 1657"> 4.electric power quality improvement by using series active and shunt passive filter. </td> <td data-bbox="1090 1585 1418 1657"> 2016-17 </td> </tr> <tr> <td data-bbox="180 1657 1090 1695"> 5.last meter smart grid embedded in an IoT Platform </td> <td data-bbox="1090 1657 1418 1695"> 2015-16 </td> </tr> <tr> <td data-bbox="180 1695 1090 1942"> 6. stator frequency regulation in a field oriented controlled DFIG connected to a dc link. 7.new modular structure DC-DC converter without electrolyte capacitors for renewable energy applications. 8.a multilevel medium-voltage inverters for step up transformerless grid connection of PV power plants. 9.an adjustable speed PFC BL buck boost converter-fed BLDC motor. </td> <td data-bbox="1090 1695 1418 1942"> 2014-15 </td> </tr> </tbody> </table>	Title	Year	1..A stand –alone PV battery powered pump system designing by using single switch dc/dc converter 2.designing of servo smart stabilizer. 3.modelling and simulation of switched inductor buck boost converter circuit for solar power system.	2017-18	4.electric power quality improvement by using series active and shunt passive filter.	2016-17	5.last meter smart grid embedded in an IoT Platform	2015-16	6. stator frequency regulation in a field oriented controlled DFIG connected to a dc link. 7.new modular structure DC-DC converter without electrolyte capacitors for renewable energy applications. 8.a multilevel medium-voltage inverters for step up transformerless grid connection of PV power plants. 9.an adjustable speed PFC BL buck boost converter-fed BLDC motor.	2014-15	
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PG projects

Title	Year
1.power flow control and analysis of transformerless UPFC.	2016-17
2.A novel series-parallel current driven full bridge dc-dc conveter.	2015-16
3. control of reduced rating DVR with battery energy storage system. 4. simulation of soft switching bidirectional isolated full bridge converter with active and passive filter. 5.back propagation control algorithm for power quality improvement using DSTATCOM. 6.Simulation of PR current controller for selective harmonic compensation in a hybrid active power filter. 7.a simulation of 3-phase 4-wire UPQC topology with reduced DC-link voltage rating.	2014-15
8.a simulation of a novel 3-phase buck boost AC-DC converter. 9.Analysis and improved operation of PEBB based VSC for FACTS applications. 10.closed loop control of DC-DC dual active bridge converters driving single phase inverters.	2013-14

Workshops/Seminars/FDP's Attended:

- 1.FDP attended in jayamukhi engg college in 2011
- 2.FDP attended in VCEW-2014.
- 3.FDP attended in VCEW in oct-2017

Conferences Attended:(preferably International)

- 1.Application of SSSC in Power Quality-2011 –KARIMNAGAR
2. DFACTS-2014-HYDERABAD.

Achievements:

- GATE RANK- 9600 -2016.
Received MHRD fellowship for a period of 2yeras.