BOBBALA SREEDEVI	

Qualification :   M.Tech (JNTU Ananthapur) (PhD - JNTU Hyderabad))

Experience: 18 years

Area of Interest: Signal and Image processing

Subjects Taught:

B Tech:

- Electronic Devices and Circuits
- Electronic Circuit Analysis
- VLSI Design
- Communication Theory
- Linear IC Application
- Communication Systems
- TV Engineering
- Wireless Communication Networks
- Digital Signal Processing
- Digital Image Processing
- Signals and systems
- Pulse and digital circuits
- Data communication systems
- Electronic circuits-II
- Analog electronics

M Tech:

- ❖ Wireless Communication Networks
- ❖ Analog IC Design
- ❖ VLSI Design
- ❖ Image and Video processing
- ❖ Design of fault tolerant systems
- ❖ Coding Theory and Techniques
- ❖ Advanced data communications

Research Publications.

- Bobbala Sreedevi, T.Anilkumar,K. Kishanrao."An Efficient Methodology for Abnormal Brain and Stage Detection in MRI Images". International Journal of Pure and Applied Mathematics Volume 118 No. 20, 2018, 1927-1933.

- Mrs.B.Sreedevi, Devunoori Mounika." Image Retrieval by Fusion of Color Histogram and Edge Orientation Histogram based on wavelet". International Journal of Research. Volume 04 Issue14, 2348-6848.

- Bobbala Sreedevi, T.Anilkumar,K. Kishanrao. "A Novel Denoising and Segmentation of Brain Tumors in MRI Images". Indian Journal of Science and Technology, Vol 9(44), DOI: 10.17485/ijst/2016/v9i44/10170.

- Bobbala Sreedevi, T.Anilkumar,K. Kishanrao "Resolution enhancement and denoising of Magnetic resonance images using SWT".International conference on emerging trends in electronics and telecommunication(ICETET 2015), ISSN No. 2394-4544.

- B.Sreedevi, B.Geetha"A Robust Image Denoising using Decimated Wavelet Thresholding Techniques". International journal of scientific engineering and technology research Vol.03,Issue.44 December -2014, ISSN 2319 -8885.

- B.Sreedevi, Vivek Bachu. "Brain Tumor Diagnosis by Contour let Coefficients Based on SGLD" International journal of scientific engineering and technology research vol.02.Issue.12, ISSN 2319-8885.

- B.Sreedevi, S.Kalyani, K.Kishanrao. "Medical Image Retrieval Using Biorthogonal Spline Wavelet". International Journal of Scientific & Engineering Research, Volume 3, Issue 11, ISSN 2229-5518.

- B.Sreedevi, V.Vijaya, Ch.Kranthi Rekha, Rama Valupadasu, B.Ramarao Chunduri "FPGA Implementation of DSSS-CDMA Transmitter and Receiver". IEEE Symposium on Computers & Informatics(ISCI 2011), Kuala Lumpur, Malaysia, PP.255-260, ISBN No. 978-1-61284-689-7.

- B.Sreedevi, V.Vijaya, Ch.Kranthi Rekha, Rama Valupadasu, B.Ramarao Chunduri. "FPGA Implementation of RS-232 to universal serial Bus converter". IEEE Symposium on Computers & Informatics (ISCI 2011), Kuala Lumpur, Malaysia PP.237-242. ISBN No.978-1-61284-689-7.

- B.Sreedevi, V.Vijaya, Ch.Kranthi Rekha" Comparison of Higher Order Diffusion Filters for Speckle Noise Reduction" International Conference on Computer Applications and Industrial Electronics (ICCAIE 2010), Kuala Lumpur, Malaysia. ISBN No.978-1-4244-9055-4/10.

No of Projects guided:	UG	PG	
	26	**10**	

Workshops/Seminars/FDP's Organized:

1. IEEE sponsored "All Hyderabad Computer Society Student Congress (AHCSSC)" from 5/1/2018 to 7/1/2018.

2. SENTIENCE IEEE awareness program one day IEEE program conducted on 10/2/2018

3. Conducted Two day national workshop on "Quad copter" from16th to 17th March 2016

4. Conducted Two day national workshop on "DSP/DIP uses LABVIEW" from 18th and 19th March 2015.

5. IEEE sponsored "Employability Skills Development "work shop conducted from 25th to 26th July 2014.

Workshops/Seminars/FDP's Attended:

➢ UGC sponsored Refresher course on "VLSI Design" from 13/11/2002 to 03/12/2002 at JNTU Hyderabad.

➢ State level Workshop on "Microprocessor & Microcontrollers "from 28/12/2004 to 29/12/2004 at Vaagdevi College of engineering, Warangal.

➢ International Symposium on IT for real world problems from 22/12/2005 to 24/12/2005 Vaagdevi College of Engineering, Warangal.

➢ Workshop on "Advanced communication equipment" conducted under TEQUIP from 3/10/2007 to 7/10/2007by NIT, Warangal.

➢ MHRD sponsored Two weeks Workshop on "Electronic Devices and Circuits" from 25/8/2008 to 6/9/2008 conducted by NIT, Warangal.

➢ Workshop on "Low Power VLSI" from 5/12/2008 to 6/12/2008 at Vaagdevi college of Engineering, Warangal in December 2008.

➢ MHRD Sponsored refresher course on "Signals and Systems and Transforms" from 9/7/2009 to 21/7/2009 at NIT, Warangal.

➢ MHRD, Govt. of India sponsored course on "Analysis and Design of Electronic Circuits" conducted by NIT, Warangal.

➢ Women in Research the Foss paradigm" Organized by UGC-Academic staff College on 26/11/2010 JNTUH, Hyderabad.

➢ Speech Signal Processing and Coding" from 19/12/2011 to 21/12/2011 Organized by NIT, Warangal.

➢ Research methodology" workshop from 16/1/2012 to 21/1/2012 conducted by JNTUH,

Hyderabad.

- National Programme on Technology Enhanced Learning A Joint Venture by Indian Institutes of Technology & Indian Institute of Science from 31/8/2012to 1/9/2012.

- A one day National work shop on "Selected topics on bio-medical image analysis'' organized by Osmania University, Hyderabad sponsored by IEEE Hyderabad Section on 18th Dec 2012.

- Course on "Image Processing and current trends" conducted by IETE, Hyderabad.

- IETE Sponsored Faculty Development Program on "DSP and its Applications from 8/2/2013 to 9/2/2013 KITs warangal.

- MHRD Sponsored two week work shop on "Signals and Systems" from 2/1/2014 to 12/1/2014 at SVS Group of institutions, Warangal.

- Section student congress 2015 "Leadership training workshop" from 3/10/2015 to 4/10/2015 at University of Hyderabad.

- One week faculty development program on "Signal processing techniques" organized by ECE department of MVSR engineering college, Hyderabad, held from 15/11/2016 to 19/11/2016.

- Meity, Govt.of India sponsored one week faculty development program on "Computer vision and image processing(CVIP)" organized by the E & ICT Academy, National Institue of Technology, Warangal at CMR Technical Campus, Hyderabad from 4/12/2017 to 9/12/2017.

Conferences Attended:

1. IEEE Computer Society India Symposium, IEM at Bodh Gaya, 16th -18th March 2018.

2. IEEE Symposium on Computers & Informatics(ISCI 2011), Kuala Lumpur, Malaysia, March 2011.

3. International Conference on Computer Applications and Industrial Electronics (ICCAIE 2010), December 5-7, 2010, Kuala Lumpur, Malaysia.