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| **SANTOSH KUMAR BODDUPELLI** | C:\Users\VCEW\Downloads\santoo1.jpg.png |
| **Qualification : M.TECH** Digital Systems and Computer Electronics (JNTU,HYDERABAD) |
| **Experience: 6 YEARS** |
| **Area of Interest:** BIOMEDICAL SIGNAL PROCESSING/INSTRUMENTATION, SMART STRUCTURE BASED SENSORS AND ACTUATORS |
| **Subjects Taught:** EMI , STLD,DLD&CO,EC,RS,SC,PDLIC,IE |
| **Research Publications:**1. B.Santosh kumar,G.Prashanth ”Low –complexity wavelet based channal estimation with low leakage for OFDM system ISSN:2320-5547 IJITR,Vol-4,IssueNo.5,September 2016.
2. B.Santosh kumar,N.Shravani “New VLSI BWA Architecture for finding the first W maximum/minimum values using sorting algorithms”ISSN(Online): 2347-4718 , IJTRE,Volume 2,August-2015.
3. B.Santosh kumar,G.Niharika ”Implementation of High Precision Fixed width Multiplier for DSP Applications” CSIT/ERKN/05/10/2402 5th October 2014.
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| **UG** | **PG** |
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**No of Projects guided:** |
| **Workshops/Seminars/FDP’s Organized:**1. PCB workshop on February ,2016
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| **Workshops/Seminars/FDP’s Attended:**1. Overview of Rasberry Pi,Aurdino microcontroller boards ,programming Applications at JNTU ,Jagityal during April 3rd -4th ,2017
2. Advanced Photonics Technologies for Science and Engineering Applications organized by OSA Student Chapter, NIT Warangal on 12th April, 2015.
3. 42nd ISTE ANNUAL CONVENTION during 20th-22nd December,2012.
4. AICTE Sponsored “Advanced Embedded And Real Time Operating Systems” Organized by Jayamukhi Institute of technological sciences, during 18th June to1st July 2012.
5. Advancedplan Ahead Techniqes Summit 2011-12, Hyderabad, India February 10,2012.
6. Three days Workshop on “SIGNALS AND SIGNAL PROCESSING” Conducted at JNTUH College of Engineering, Hyderabad during 28th to 30th June-2010.
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| **Conferences Attended:**1. Presented paper “Design and Development of PCI Based add-on card for Data Acqisition” in the National Conference on “RECENT TRENDS AND ADVANCES IN NANO TECHNOLOGY” on 28th & 29th July 2011.
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