Sudheer Raja Venishetty	

Qualification :

M.Tech (Digital Systems and Computer Electronics)
(PhD - Pursuing at VIT University, Vellore)

Experience:

-Worked as Assistant Professor At Vaagdevi College of Engineering from June 2003 to February 2012
- worked as Assistant Professor at Adama Science and Technology University, Adama , Ethiopia from March 2012 to October 2014.
-Presently working as Associate Professor at Vaagdevi College of engineering since October 2014.

Area of Interest:

Analog and Digital VLSI Design,

Subjects Taught:

VLSI Design, STLD ,LDIC ,EDC ,Low power VLSI , CO,Control Systems, Digital System Design , Design of Fault Tolerant Systems , DICA , LICA , Analog and Digital communications(Ethiopia) , MICROBWAVE engineering
(Ethiopia) ,

Research Publications:

1. G Sai Manoj, B.Sravanthi, G.Thirumal, Sudheer Raja Venishetty."VLSI IMPLEMENTATION OF SMS4 CIPHER FOR OPTIMIZED UTILIZATION OF FPGA",2nd International Conference on Inventive Communication and Technologies (ICICCT 2018) organized on 20-21, April 2018 at Coimbatore (IEEE DOI is not yet Allotted)

2. V. Sudheer Raja , Dr S Kumaravel ."Design of Recycling Folded cascode Amplifier Using Potential Distribution Method" at IEEE International Conference on microelectronics Devices, circuits and systems (ICMDCS'17), at VIT University, Vellore , 11th to 12th August 2017, IEEECatalogNumber:CFP1329U-ART ISBN: 978-1-5386-1716-8

3. V. Sudheer Raja, G.Subbarao, Aman Miesso Bokiye. " *FPGA Implementation of MAC   based Stream Cipher-Phelix*", International Conference on Machine Intelligence Research and Advancement ,ICMIRA-2013 With Proceedings from IEEE CPS, at SMVD University, Katra Jammu India during 21st – 23rd December 2013.IEEE Computer Society Order Number E5013 , IEEECatalogNumber:CFP1329U-ART ISBN: 978-0-7695-5013-8.

4. V.Sudheer Raja, Dr.M.V.Raghavendra, G.Subba Rao "*Simulation of PCI Express Transaction layer using Hardware description language*", The Indian Journal of Engineering, Vol .1,No.1,Nov 2012, India.ISSN: 2319-7757 (print) ISSN: 2319-7765 (online)

5. V.Sudheer Raja, M.Shahsidhar, B.Vijay Kumar, CH.Anil kumar "*Logarithm and Antilogarithm Computations for Floating point Numbers Based on Linear Interpolation*", IEEE National Conference on *"Emerging Trends and Applications in Computer science"*., NCETACS-2012 at St. Anthony's College Shillong, Meghalaya during 30th- 31st March 2012.

IEEE Catalog Number: CFP1258M-ART ,   ISBN: 978-1-4577-0748-3

6. V.Sudheer Raja, M.Shahsidhar, B.Vijay Kumar, G.Madhuri   "*Performance Analysis of Adaptive Algorithms for Noise Cancellation*",   The IEEE International Conference on   Computational

Intelligence and Communication Networks, CICN-2011 Gwalior, India

IEEE Catalog Number: CFP1127L-PRT ,   ISBN: 978-0-7695-45

7. V.Sudheer Raja, M.Shahsidhar, B.Vijay Kumar, Prof D.V.Krishna Reddy, "*Textual encryption using Conventional Encryption algorithm".*2011 International Conference on Network Communication and Computer, ICNCC 2011, New Delhi, India

IEEE Catalog Number: CFP1150M-PRT , ISBN: 978-1-4244-9551-1

No of Projects guided:	UG	PG	
	15	5	