SWATEJO RANADHEER CHANDA

Qualification : M.TECH IN VLSI SYSTEM DESIGN

Experience : 7 years

Area of Interest: VLSI DESIGN

Subjects Taught:

1.ELECROMAGNETIC WAVES AND TRANSMISSION LINES
2.LINEAR INTEGRATED CIRCUITS AND APPLICATIONS ,
3.VERY LARGE SCALE INTEGRATION DESIGN
4.CELLULAR MOBILE COMMUNICATION ,
5.RADAR SYSTEMS,
6.TELEVISION ENGINEERING,
7.PROBABILIY THEORY &STOCHASTIC PROCESSES,
8.COMPUTER ORGANIZATION AND ARCHITECTURE,
9.DIGITAL IMAGE PROCESSING ,
10.TELECOMMUNICATION SWITCHING SYSTEMS AND NETWORKS,
11.EMBEDDED SYSTEMS

Research Publications:

JOURNAL:

1. CH .S.Ranadheer, Ramesh Jitty (2014) "Implementation Of Pseudo-Random Sequence Generator" International Journal Of Advanced Research and Innovation (IPHV7I10022X).

2. CH .S.Ranadheer,varun tej(2017)"Implementation Of Finite multiplier using Rb over Gf" journal for studies in management and planning(ISSN:2395-0463)

No of Projects guided:

UG	PG
12	1

Workshops/Seminars/FDP's Organized:

- I have organized a three days national level work shop on "**Prabhignan workshop"** which is conducted by department of ECE Warangal Institute of Technology And Science ,Warangal.

- I have organized a three days national level work shop on **"vijnana 14"** which is conducted by department of ECE, Trinity College Of Engineering and Technology .
- I have organized a two days national level work shop on "**Home Automation Using Simple Electronic Circuits impletation on PCB**" which is conducted by the department of ECE, Vaagdevi College Of Engineering, Warangal.

Workshops/Seminars/FDP's Attended:

- I Have Attended **'SPICE AND MAT LAB SIMULATIONS'** conducted by the department of ECE Ramappa Engineering College, Warangal.
- I have attended the work shop on '**Recent Trends In Embedded Systems'** conducted by the Department of ECE Jyothismathi Institute Of Technology And Science, karimnagar.
- I have attended the work shop on **'Digital Signal Processing & Applications'** at kakathiya Institute Of Technology And Science , Warangal
- I have attended the **'Faculty Development Program'** on teaching skills at Warangal Institute of Technology And Science, Warangal.
- I have attended the **'Faculty Development Program'** on VLSI which is conducted by department of ECE Trinity College of engineering and Technology karimnagar.

Achievements:

- I am a topper in my post graduation.
- I received a medal as a player in faculty cricket tournament conducted by trinity college of engineering & technology, Karimnagar.