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| **UDUTHA SARITHA** | C:\Users\Vijay\Desktop\nba work 2017\WEB SITE\photos\5-156.jpg |
| **Qualification:**M.Tech (VLSI SYSTEM DESIGN)- KAKATIYA UNIVERSITY |
| **Experience:**I'm working as a assistant professor in vaagdevicollege of engineering from January 2017 to till date. |
| **Area of Interest: Analog and digital electronics** |
| **Subjects Taught: Computer organization** |
| **No of Projects guided:**

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| **UG** | **PG** |
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