

**D. R. RAMJI**



**Qualification : B.E., M.E., Ph.D (Anna university)**

**Experience: 8 Years 9 Months**

**Area of Interest: VLSI, Image Processing, IoT**

**Subjects Taught:**

**VLSI Design, Microprocessors and Microcontrollers, Electronic Devices and Circuits, LIC, Signals and Systems, ASIC and FPGA Design, SOC Design**

**Research Publications:**

- 1. RAMJI, DR & PARIMALA GEETHA, K 2015, 'OPTIMAL SAMPLING PATTERN FOR EXTRACTION OF QUALITY IMAGE FROM CFA WITH COLOR ARTIFACTS', JPAM, SPECIAL ISSUE ON RECENT RESEARCH CHALLENGES IN BIO-MEDICAL APPLICATIONS, VOL. 9, PP. 209-215, ISSN : 0973-7510 (ANNEXURE I). IF – 0.073**
- 2. RAMJI, DR, et.al., "A SURVEY ON ADAPTIVE EDGE-ENHANCED COLOR INTERPOLATION PROCESSOR FOR VLSI ARCHITECTURE" IN IJOER ISSN: 2321-7758, VOL.2., ISSUE.6, 2014 IF – 4.349**
- 3. RAMJI D.R, DR. et.al., A SURVEY PAPER ON DATA HIDING TECHNIQUE BASED ON CODEWORD SUBSTITUTION ALGORITHM IN IJOER ISSN: 2321-7758, VOL.1., ISSUE.5, 2015 IF – 4.349**

**No of Projects guided:**

<b>UG</b>	<b>PG</b>
<b>12</b>	<b>6</b>

**Workshops/Seminars/FDP's Organized: 2**

**Workshops/Seminars/FDP's Attended: 10**

**Conferences Attended: 3**