

## **EXAMINATION BRANCH** VAAGDEVI COLLEGE OF ENGINEERING



## **AUTONOMOUS**

BOLLIKUNTA, KHILA WARANGAL(MANDAL) WARANGAL URBAN - 506 005 TIME TABLE FOR III -B.TECH II - SEMESTER (R14) SUPPLEMENTARY EXAMINATIONS DECEMBER 2019

TIME.	02 00	DIVI	OF C	O DAG
TIME:	02.00	FIVI -	U5.U	JU PIVI

		TIME: 02.00 PM - 05.00 PM									
BRANCH		DATE / DAY									
	06.12.2019 FRIDAY	10.12.2019 TUESDAY	13.12.2019 FRIDAY	17.12.2019 TUESDAY	20.12.2019 FRIDAY	24.12.2019 TUESDAY	28.12.2019 SATURDAY				
CIVIL ENGINEERING (01-C E)	DESIGN OF STEEL STRUCTURES	GEO TECHNICAL ENGINEERING	HIGHWAY ENGINEERING	STRUCTURAL ANALYSIS – II	DISASTER MANAGEMENT	ENVIRONMENTAL IMPACT ASSESSMENT & MANAGEMENT					
ELECTRICAL & ELECTRONICS ENGINEERING (02 – EEE)	POWER SEMICONDUCTOR DRIVES	SWITCH GEAR AND PROTECTION	MANAGERIAL ECONOMICS & FINANCIAL ANALYSIS	RENEWABLE ENERGY SOURCES	UTILIZATION OF ELECTRICAL ENERGY						
MECHANICAL ENGINEERING (03 – MECH)	FINITE ELEMENT METHODS	DESIGN OF MACHINE MEMBERS-II	HEAT TRANSFER	REFRIGERATION & AIR CONDITIONING	AUTOMOBILE ENGINEERING	NON CONVENTIONAL SOURCES OF ENERGY					
ELECTRONICS & COMMUNICATION ENGINEERING (04 – ECE)	MICROPROCESSORS & MICROCONTROLLERS	ELECTRONIC MEASUREMENTS & INSTRUMENTATION	DIGITAL SIGNAL PROCESSING	TELECOMMUNICATION SWITCHING SYSTEMS & NETWORKS	EMBEDDED SYSTEMS & PROGRAMMING						
COMPUTER SCIENCE & ENGINEERING (05 – CSE)	DATA WAREHOUSING AND DATA MINING	SOFTWARE TESTING METHODOLOGY	E-COMMERCE	ADVANCED DBMS	DISASTER MANAGEMENT	.NET TECHNOLOGIES / COMPTER GRAPHICS					

ADDL CONTROLLER OF EXAMINATIONS

CONTROLLER OF EXAMINATIONS

PRINCIPAL