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Hardware Modeling Using Verilog

with a consolidated score of **46** %

Online Assignments	16.34/25	Proctored Exam	30/75

Total number of candidates certified in this course: **336**

A. Goswami

Prof. Adrijit Goswami
Dean, Continuing Education & NPTEL Coordinator
IIT Kharagpur

Aug-Oct 2019
(8 week course)

Indian Institute of Technology Kharagpur

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To validate and check scores: https://nptel.ac.in/noc