



VAAGDEVI COLLEGE OF ENGINEERING

Affiliated to JNTUHyderabad

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

AICTE Sponsored FDP on DSP Application to Industrial Drives.

Electrical & Electronics Engineering Department has organized a Two Week FDP on “**DSP Application to Industrial Drives**” dated 18-11-2013 to 30-11-2013 by Dr. K Prakash and P Sadanandam for Faculty of the college and other AICTE approved colleges. Dr. V. T. Somasekhara has given a lecture series and demonstration on various DSP applications to Industrial Drives. No of Participants Faculty Participants: 64

The programme is started with welcome note by the Principal **Dr. Ch. Sathaiah** followed by the lecture session.

BRIEF REPORT ON AICTE SPONSORED FACULTY DEVELOPMENT PROGRAMME

ON

“DSP APPLICATIONS TO INDUSTRIAL DRIVES”

Introduction

Power Electronic Devices are finding its applications in commercial and industrial distribution systems. If Customized with DSP power devices are used in applications such as voltage control, speed control, power quality improvement and distributed power generation. Recent advances in power electronic converters with space vector modulation, multi-phase techniques, multi-pulse techniques of output control has led to reduced filter requirement and DSP control techniques. These advances have found power electronics applications in FACTS devices used under high voltage system, smart grid, distributed generation etc.

Power Electronic converters are nonlinear in nature and cause many power quality problems. In addition they are sensitive to power quality disturbances which lead to mal-operation or damage of the sensitive equipment. Power quality disturbances such as voltage sag, swell, harmonics, transients and noise are to be monitored and suitable mitigation action to be taken. Advances in power electronic converters enhanced with DSP leads to intelligent design, modeling and analysis of various applications which are in progress which result into less harmonic currents and desirable power factor operation.

Objective of the Programme

The main objective of this FDP is to introduce recent developments in Digital signal processing (DSP) and its applications in the power electronic converters, motor drives with emphasis on DSP, application of space vector based PWM Techniques multilevel Inverters, Direct torque control and space based electric drives, harmonic cancellation, converters (AC/DC, DC/DC, DC/AC, AC/AC Frequency changer), power electronic data acquisition their topologies and control techniques, multi-phase inverter, PWM-controlled rectifiers, Converters used in distributed generation and custom power devices used for power quality improvement. Several case studies presented in this FDP helped the faculty to gain in depth knowledge with emphasis to research. Laboratory sessions are arranged to provide hands on experience. This FDP aims to give exposure to possible research in this area with emphasis on Research, with case studies and technical information. This gathering with multidimensional approach is the great opportunity for faculty to share their proficiency and promote their research and industrial skills. Resource persons being from IITs, NITs and other premier institutions contributed for this objective.

Programme Schedule

The programme **DSP APPLICATIONS TO INDUSTRIAL DRIVES** starting 18th November to 30th November 2013 is designed in such a way that, the participant starts with concept of Power Electronics Converters & Digital signal processing, their advances and control algorithms for output control and changes in converter topologies to improve efficiency. Introduction of Digital signal processing lab sessions both simulation and practical in power electronics and Digital signal processing laboratory dealing with mitigation of several power quality disturbances were also covered during this 2-week FDP. The detailed Programme Schedule is enclosed in Annexure-I.

Execution of the Programme

With the above objective in mind, EEE Department of Vaagdevi College of Engineering has applied to All India Council for Technical Education (AICTE) under Faculty Development Programme Scheme by the theme "**DSP APPLICATIONS TO INDUSTRIAL DRIVES**" during 18th November to 30th November-2013. AICTE has approved the Programme and sanctioned an amount of Rs. 4, 00, 000/- (Rupees Four Lakhs only) in the month of November, 2013. Details of Faculty Development Programme in the form of Brochure were communicated in the second week of October 2013 to 500 Engineering Colleges throughout the country, out of which 80 applicants responded within the stipulated time (13-11-2013) of which sixty four (64) participants were selected and confirmed by E-mail and Telephone. Details of Participants are enclosed in Annexure-II.

Resource Persons

The resource persons with Digital signal processing, Electrical Power System, Power Electronics and relevant specializations are mainly drawn from different institutions and industry like IIT Madras, IIT Hyderabad, NIT Warangal, JNTU Hyderabad, JNTU Jagtiala, Osmania University Hyderabad, apart from internal faculty to meet the theme "**DSP APPLICATIONS TO INDUSTRIAL DRIVES**".

Dr V.T. Somasekhar, Professor, Dept. of EE, NIT Warangal delivered the keynote address on Inaugural session. The detailed schedule of resource persons and their topics presented date wise are attached in annexure-I.

Course Material Details

The course material is prepared in two volumes. The first one "**DSP APPLICATIONS TO INDUSTRIAL DRIVES**" is exclusively prepared for familiarizing the participants in the Fundamentals of Power Electronics Converters such as Rectifier, Inverter, Dc-Dc converters, PWM techniques, Introduction of applications and scope of DSP to Distributed generation such as solar, wind and small hydro generation with applications of power electronics in these areas, Power quality issues – Monitoring, ANN techniques, Fuzzy control techniques, Effects of Power quality problems and mitigation methods. Also theory and step by step procedure of software tools such as PSCAD, MATLAB-Simulink and MiPower are introduced. It is published within a DVD were handed over on valedictory function. The second one contains the lecture notes of the invited outstation resource persons and in-house resource persons. It is published in soft bound fashion. The above two modules along with 100 page long note book with pen and the entire program schedule and list of participants with address details, photo gallery, resource persons details, were handed over in an executive bag to the participants on the inaugural day i.e. 18th November, 2013.



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
VAAGDEVI COLLEGE OF ENGINEERING, WARANGAL
AICTE Sponsored Faculty Development Programme
DSP APPLICATIONS TO INDUSTRIAL DRIVES
(18th November to 30th November, 2013)



AGENDA

1. Welcome by Mr. T. Venu Gopal, Asst. Prof., EEE.
2. Jyothi Prajwalana
3. Invocation
4. Opening Remarks by President Prof. Ch. Sathiah, Principal, VCEW.
5. Report by Dr. K. Prakash, Coordinator
6. Address by Chief Administrative Officer Sri. C. Satyapal Reddy, Vaagdevi Group of Technical Institutions.
7. A) Introduction of Chief Guest by Mr. N. Mahender, Asst. Prof., EEE.
B) Address by Chief guest, Dr. V.T. Somasekhar, NIT, Warangal.
8. Felicitation to chief guest Dr. V.T. Somasekhar, NIT, Warangal.
9. Vote of thanks by Mr. P. Sadanandam, Co-coordinator.

Inaugural and Lecture Sessions

- FDP started with an inauguration on 18th November, 2013 with *Dr. V.T. Somasekhhar*, Prof., NIT, Warangal as the chief guest. *Dr. Ch.Sathiah*, Principal, VCEW and *Dr. K.Prakash*, Coordinator of this FDP and HOD, EEE Dept. were present during the inauguration. Faculty of EEE Dept. and Heads of other departments were present.
- On 18th November *Dr.P.Srinivas* *Osmania University Hyderabad* has given lecture on Modeling and control of Switched Reluctance Drive.
- On 18th November *Mr.E.Vidya Sagar*, *Osmania University Hyderabad* has given lecture on Enhancing Integration of Renewable in Distribution Systems through Smart Links.
- On 19th November *Prof. V.T. Somasekhhar* of NIT Warangal who was the Chief Guest for the Inaugural function has delivered lecture on Implementation of SVPWM Technique with 320LF2407A Processor.
- On 19th November *Prof. V.T. Somasekhhar* of NIT Warangal who was the Chief Guest for the Inaugural function has delivered lecture on Multilevel Topologies and Open end winding IM Drives.
- On 19th November *Prof. V.T. Somasekhhar* of NIT Warangal who was the Chief Guest for the Inaugural function has delivered lecture on Elimination of zero sequence currents in open end winding IM Drives.
- On 19th November *Prof. V.T. Somasekhhar* of NIT Warangal who was the Chief Guest for the Inaugural function has delivered lecture on Vector control of IM with new 3-level Inverter.
- On 20th November *field trip* Was organized to 400Kv unmanned substation of power grid corporation of India.Ltd permission letter enclosed in Annexure -V
- On 21st November, *Dr.S.Srinivas*, of IIT, Madras has given lecture on PWM methods, implementation and implications
- On 21st November, *Dr.S.Srinivas*, of IIT, Madras has given lecture on Multilevel Inverter and multilevel PWM methods.
- On 21st November, *Dr.S.Srinivas*, of IIT, Madras Lab session.
- On 22nd November *Dr. K. Nagasujatha*, JNTUCEH, Jagityala. has presented a session on Direct Torque Control of Induction Motor Drives.
- On 22nd November *Dr. K. Nagasujatha*, JNTUCEH, Jagityala has presented a session on Adaptive controllers.
- On 22nd November *Mr.K.Srinivas*, JNTUCEH, Jagityala has presented a session on Design and implementation of Active Power Filter for Power Quality improvement.
- On 23rd November *Dr.M.Sydulu*, NIT, Warangal has presented a session on Research Methodologies.
- On 23rd & 24th November *Dr.K.Shiva Kumar* IIT, Hyderabad has presented a session on Architecture of DSP TMS 320F2407, Important Peripherals of DSP Processor, Cascaded multi modular multilevel inverters (review), Development of multilevel inverter from basic blocks, Implementation of Asynchronous PWM technique for multilevel inverters using TMS 320F2407/2812.

- On 25th & 26th November *Dr.N.Vishwanathan, NIT, Warangal* has presented a session on Resonant converters, Phase Modulated Converters, Induction Heating-I, Induction Heating -II, Tri Dimensional power converters, Solar Power connected to Grid, HVDC Power Supplies, LED Lighting.
- On 27th November *Dr.S.Srinivasa Rao, NIT, Warangal* has presented a session on Maximum Power Tracking Techniques for Wind Energy Systems, Research Trends in Solar Photovoltaic Technology, Research Trends in Chemical Sources of Energy (Fuel Cells), Signal Processing Techniques, Power System Harmonics and Mitigations
- On 28th November, *Dr.Srinivas Vatsal, Rt. Scientist, DRDO, New Delhi* has presented a session on Kalman filter on Power Electronics and Power System Applications.
- On 28th November, *Dr.K.Prakash, VCE, Applications to Metauristic Techniques.*

Some Important Photographs of inauguration and rest of the program are enclosed in Annexure-III.

Press clippings are enclosed in Annexure-IV

Valedictory Function was arranged on 30th November afternoon. Certificates and FDP Sessions CDs were issued to participants.

Valedictory function and feedback oral and written were collected sample format enclosed in Annexure-V

Receipts for the DD's from the participants were collected and acknowledgement of receipts given sample format enclosed in Annexure -V

Certificate of participation were distributed by the chief guest enclosed in Annexure-V

Boarding and Lodging Arrangements to Participants

All the participants were accommodated in Star lodge at Hanamkonda. Double rooms were provided for all participants. Lunch arranged for all resource persons, supporting staff along with participants in the dining hall. Dinner arranged only for participants in the hotel itself.

Valedictory Session

Valedictory session was organized on the last day of the programme i.e. 30-11 - 2013 in the Vaagdevi College of Engineering (VCE) Auditorium in A- block. *Dr. S.Vathsal*, Retired Scientist, DRDO was the Chief Guest of the function. *Prof. K.Kishan Rao*, Director, Vaagdevi Colleges addressed the gathering. *Dr. K.Prakash*, coordinator spoke on this occasion. Some of the participants responded with their feedbacks highlighting the pros and cons of the programme. All feedbacks were found to be satisfactory. A sample feedback form is attached in Annexure-V. Certificates to the participants were distributed by *Dr. S.Vathsal* and *Prof. Ch.Sathaiah*, President of Valedictory function Participation certificate distributed to the participants enclosed in Annexure-VI. The programme was concluded with Vote of Thanks by *Sri. P.Sadanandam*, Co-Coordinator, Associate Professor, VCEW.

ANNEXURE-1
Program Schedule
ACTE SPONSORED FACULTY DEVELOPMENT PROGRAMME SCHEDULE
IASP APPLICATION TO INDUSTRIAL DIVISION No. 0.97 Nov. 2021
WEEK - 1

S. No	Time/Date	08.30AM to 11.00AM	11.00AM to 12.40PM	12.40PM to 1.30PM	1.30PM to 3.40PM	3.40 PM to 3.50 PM	3.50PM to 4.40PM
1	18/11/2021 (Monday)	Integration session	Modeling and control of Sawtooth Inverter (Dr. P. A. Vinod O.U. Hyderabad)	Modeling and control of Switched Reluctance Drive (Dr. P. Srinivas O.U. Hyderabad)	Elimination of zero sequence currents in open set winding IM Drives (Dr. V. T. Srinivasulu NIT, Wg.)		Extensive Integration of Renewable Distribution Systems through Smart Grids (Dr. E. Vidya Sagar, O.U. Hyderabad)
2	19/11/2021 (Tuesday)	Implementation of SVM-PWM technique with 120° FEA Topic: (Dr. V. T. Srinivasulu NIT, Wg.)	Multi-level Topologies and Open ended winding IM Drives (Dr. V. T. Srinivasulu NIT, Wg.)	Field Trip Power Grid Corp 400kV unarmoured Substation T. Anveshan (Dr. S. Srinivasulu, IIT Madras)	Vector control of IM with three phase inverter (Dr. V. T. Srinivasulu NIT, Wg.)		
3	16/11/2021 (Wednesday)	Power Grid Corp 400kV unarmoured Substation T. Anveshan (Dr. S. Srinivasulu, IIT Madras)	Field Trip Power Grid Corp 400kV unarmoured Substation T. Anveshan (Dr. S. Srinivasulu, IIT Madras)	Design and implementation of Active Power Filter for Power Quality Improvement (Dr. K. Srinivasulu, IIT Hyderabad)			
4	21/11/2021 (Thursday)	Power Grid Corp 400kV unarmoured Substation T. Anveshan (Dr. S. Srinivasulu, IIT Madras)	Field Trip Power Grid Corp 400kV unarmoured Substation T. Anveshan (Dr. S. Srinivasulu, IIT Madras)	Design and implementation of Active Power Filter for Power Quality Improvement (Dr. K. Srinivasulu, IIT Hyderabad)			
5	22/11/2021 (Friday)	Direct current Control of Inverter Motor Drives (Dr. T. Nagarajulu, JNTUCC, Hyderabad)	Acaptive converters (Dr. K. Nagarajulu, JNTUCC, Hyderabad)	Design and implementation of Active Power Filter for Power Quality Improvement (Dr. K. Srinivasulu, IIT Hyderabad)			
6	23/11/2021 (Saturday)	Research Methodologies (Dr. M. Sridhar, NIT, Wg.)	Architecture of FSP TMS 320F2407 (Dr. K. Srinivasulu, IIT, Hyderabad)	Important Peripherals of DSP Processor (Dr. K. Srinivasulu, IIT, Hyderabad)			
7	24/11/2021 (Sunday)	Development of multi-level Inverter from basic blocks (Dr. K. Srinivasulu, IIT, Hyderabad)	Development of multi-level Inverter from basic blocks (Dr. K. Srinivasulu, IIT, Hyderabad)	Implementation of asynchronous PWM technique for multi-level Inverters using TMS 320F2407/2812 (Dr. K. Srinivasulu, IIT, Hyderabad)			

TEA BREAK

TEA BREAK

WEEK - 2

S. No	Topic/Date	9:30 AM to 11:00 AM	11:00 AM to 11:10 AM	11:10 AM to 12:40 PM	12:40 PM to 1:30 PM	1:30 PM to 3:00 PM	3:00 PM to 3:10 PM	3:10 PM to 4:40 PM
8	25/11/2013 (Monday)	Resonant converters (Dr. N. Vigneshwaran NIT, Wg)	TEA BREAK	Phase Modulated Conversion (Dr. N. Vigneshwaran NIT, Wg)	Induction Heating - I (Dr. N. Vigneshwaran NIT, Wg)	Induction Heating - II (Dr. N. Vigneshwaran NIT, Wg)	TEA BREAK	Induction Heating - III (Dr. N. Vigneshwaran NIT, Wg)
9	26/11/2013 (Tuesday)	Tri/Direct/indirect power converters (Dr. N. Vigneshwaran NIT, Wg)	Solar Power connected to Grid (Dr. N. Vigneshwaran NIT, Wg)	HYDC Power Supplies (Dr. N. Vigneshwaran NIT, Wg)	LED Lighting (Dr. N. Vigneshwaran NIT, Wg)	Power System Harmonics and Mitigations (Dr. D. V. S. Siva Sarma NIT, Wg)	TEA BREAK	Power System Harmonics and Mitigations (Dr. D. V. S. Siva Sarma NIT, Wg)
10	27/11/2013 (Wednesday)	Maximum Power Tracking Techniques for Wind Energy Systems (Dr. S. Shrivasa Rao, NIT, Wg)	1. Research Trends in Solar Photovoltaic Technology 2. Research Trends in Chemical sources of Energy (Fuel Cells) (Dr. S. Shrivasa Rao, NIT, Wg)	Signal Processing Techniques (Dr. D. V. S. Siva Sarma NIT, Wg)	Applications in Measurables Techniques (Dr. K. Pradeep, VCE, Wg)	Modeling and Simulation of Power Electronic Converter (Dr. D. Vignesh Reddy NIT, Goa)	TEA BREAK	Modeling and Simulation of Power Electronic Converter (Dr. D. Vignesh Reddy NIT, Goa)
11	28/11/2013 (Thursday)	Karman filter on Power Electronics and Power System Applications (Dr. Shrivasa Vaseel Rajasekhar, DRDO, New Delhi)	Karman filter on Power Electronics and Power System Applications (Dr. Shrivasa Vaseel Rajasekhar, DRDO, New Delhi)	Lab Session (Dr. B. Vignesh Reddy, NIT, Goa & Mr. P. Subramanian, VCE, Wg)	Lab Session (Dr. B. Vignesh Reddy, NIT, Goa & Mr. P. Subramanian, VCE, Wg)	Lab Session (Dr. B. Vignesh Reddy, NIT, Goa & Mr. P. Subramanian, VCE, Wg)	TEA BREAK	Lab Session (Dr. B. Vignesh Reddy, NIT, Goa & Mr. P. Subramanian, VCE, Wg)
12	29/11/2013 (Friday)	4-Level Inverter for Open End winding Induction Motor Drive (Dr. B. Vignesh Reddy NIT, Goa)	4-Level Inverter for Open End winding Induction Motor Drive (Dr. B. Vignesh Reddy NIT, Goa)	Research Experiences Dr. Shrivasa Vaseel	Research Experiences Dr. Shrivasa Vaseel	Research Experiences Dr. Shrivasa Vaseel	TEA BREAK	Research Experiences Dr. Shrivasa Vaseel
13	30/11/2013 (Saturday)	Project Formulation Dr. Shrivasa Vaseel	Project Formulation Dr. Shrivasa Vaseel	Project Formulation Dr. Shrivasa Vaseel	Project Formulation Dr. Shrivasa Vaseel	Project Formulation Dr. Shrivasa Vaseel	TEA BREAK	Project Formulation Dr. Shrivasa Vaseel

ANNEXURE-II

**DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
VAAGDEVI COLLEGE OF ENGINEERING, WARANGAL**

**AICTE Sponsored Faculty Development Programme
DSP APPLICATIONS TO INDUSTRIAL DRIVES**

List of Faculty Members Registered for Two week FDP (18th Nov to 30th Nov, 2013)



Sl.No	Name	Designation	Address	Contact	E-mail ID
1	Mr. S. Sridhar	Asst. Prof.	Bhagy Institute Engineering and Sciences	9636061616	singam_sridhar@yahoo.com
2	Mr. V. Mani Krishna	Asst. Prof.	Bhagy Institute Engineering and Sciences	9895864711	bramankoor@gmail.com
3	Mr. V. Balaramu	Teaching Assistant	Sri Venkateswara University	9492629266	Vbtraja1pt@gmail.com
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5	Mr. J. Anjali	Asst. Prof.	Princeton Institute of Engineering and Technology for women, Karimnagar	9908327983	Anj202@gmail.com
6	Mr. P. Shiva Krishna	Asst. Prof.	KVR&JC College of Engineering, Guntur	9490443481	p.shivakrishna@gmail.com
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9	Mr. R. Subhash	Asst. Prof.	KUC&T, Warangal	9720366054	subhash31@yahoo.co.in
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14	Mr. Anjanayulu	Lecturer	VMR Polytechnic, Warangal	9652429570	127anjanu@gmail.com
15	Mr. M. Suresh	Asst. Prof.	Sindhu College of Engineering	---	---
16	Mr. P. Madhavi	Asst. Prof.	VMR Polytechnic, Warangal	966887544	Madhavi212@gmail.com
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22	Mr K Ram Mohan Reddy	Assoc Prof	Surathi Reddy Institute of Technology for Women	8686555345	kranmohareddyec@gmail.com
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25	Mr K Vamsi Chari	Asst.Prof	Ganapathi Engineering College	9030902732	Vamsadhar2005@gmail.com
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49	Mr. E. Arun	Asst Prof	Balaji Institute Engineering and Sciences, Narasimpet	9333399465	egopalapillaiarun@gmail.com
50	Mr. T. Hari Krishna	Asst Prof	Balaji Institute Engineering and Sciences, Narasimpet	8299812419	t.h.krishna1989@gmail.com
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52	Mr. P. Shilpa	Assoc. Prof.	Puthinder College of Engineering	9929949123	potharajushilpa@gmail.com
53	Mr. M. Kumaraswamy	Asst. Prof.	Ganapathy College of Engineering	9640326321	-----
54	Mr. M. Narash	Asst. Prof.	Ganapathy College of Engineering	9029389030	---
55	Mr. Md. Azharuddin	Asst. Prof.	Ganapathy College of Engineering	9885065768	Azharuddin208@gmail.com
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59	Mr. B. Sarpath	Asst. Prof.	Sinchra College of Engineering	---	---
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62	Mr. S. Shubhash	Sr. Asst. Prof.	S.R. Engineering College	9866376101	satishbocba@gmail.com
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Annexure-III

Important Photographs



Dr. V.T. Somasekhar, NIT Warangal Chief Guest
Inaugural Function of the FDP



18-11-2013 Mr. E. Vidyasagar, Osmania University Clg. of Engg.



19-11-2013 Dr. V.T. Somasekhar, NIT Warangal



19-11-2013 Dr. V.T. Somasekhar, NIT Warangal



26-11-2013 Field trip to Power Grid Corporation



21-11-2013 Dr.S.Srinivas, IIT, Madras



22-12-2013 Dr.K.Nagasujatha, JNTUCEH, Jagityal



23-11-2013 Dr.K.Siva Kumar, IIT, Hyderabad



25&26-11-2013 Dr. N.Vishwanathan, NIT Warangal



26-11-2013 Dr.S.Vathsal Former Scientist



28-11-2013 Dr. K. Prakash, VCEW



Photograph of the Participants of the FDP

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హాద్యుమతి, సోమశతర్

ಪ್ರಾಚೀನ ಕಾಲದಿಂದಲೂ ಇಲ್ಲಿಯವರೆಗೆ
ಮಾಡಿದ ಕೆಲಸಗಳನ್ನು ಮುಂದುವರಿಸಿ
ಮಾಡುವ ಕೆಲಸವನ್ನು ಮಾಡುವ ಕೆಲಸವನ್ನು
ಮಾಡುವ ಕೆಲಸವನ್ನು ಮಾಡುವ ಕೆಲಸವನ್ನು

Andhrajyothi 19-11-2013

EENADU 30-11-2013

శాస్త్రీయ పరిశోధనలతోనే సమాజాభివృద్ధి

దానిమిద(నగరిం), నూగ్గియిన్ : కాన్సేసు
వరకొనవలసివచ్చి నమూనామీసెన్ సాగ్గ
మని, ఇందుకు ప్రతిపాదకకాపాసు మిరాన్సాన్
మని ముఖ్యకాగ్గ వేయించునని దిగ్గిగ్గ కాన్స
వేగ్గ మూర్తీగ్గ కిరీవేలీ నమూనావలె దాన్గ
క్రీమిదాని సగ్గగ్గ అయిదు. మిరదించున
దానిమిదం దాన్గ్గ ఇదెవరినోగ్గ కళాకారి
వదినను గీరాలగా అదాన్యమని సాచి
వేగ్గ శ్లేషా గిరాలం 'దేవినీ అగ్గివేయ్గ్గ
అందమగ్గ దాన్గ్గ' అని అందంపై నెరుగు
నూర్తాచి. వేగ్గదాని శ్లేషా నగిరదం
మూసావరిగిగా వేగ్గనీను అయిన నాగ్గ
దానిమిదా ప్రవృత్త వేగ్గదానిగ్గ నమూనా
మామిదు అననమని, గిగ్గ కారం వేగ్గని,
40 కారం వేగ్గనీవేగ్గ మురం అగ్గివేయ్గ్గ
నెరుగివేగ్గగ్గ వేగ్గదాని. నాగ్గి వేగ్గదాని
సరిదానిగ్గ కిరీవేలీవలె మూగ్గదాని అద



శిర్డీ శివరంలి మాట్లాడుతున్న శ్రీనివాసుని పత్తిలో

విశయగంలో ఓమ్మి అష్టపద్యమి ఎంతో ప్రాధాన్యం పెడించెను. ఇంద్రగిరిలోపాలు నువ్వేమి, గృహపరిరాలు, శక్తియవరంగా ప్రెరియ దిక్పాలిలో పాంపెరిరంగా గాంతో వాదం పెడించెనాని, ప్రెరిపాం పర్యయ మోటాదుతో మిరి, ఎంతో

ప్రాధమిక విద్యను పూర్తి అభ్యసించిన వీరప్ప
మొదట అంబేద్కర్ ప్రభుత్వం అందజేసిన మోక్ష
గుం ఫ్రెంచ్ అధ్యయనానికి మార్గదర్శక, వీరప్ప
హామిలె కె. నూనా, నవనీరప్ప హామిలె మొదల
రావించిన అభివృద్ధి పథకం పై పనిచేసినట్లు
మీదిని రాజామహేంద్రవరం పోలీస్ స్టేషన్

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Participants Certificate



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