VAAGDEVI COLLEGE OF ENGINEERING

Affiliated to JNTUHyderabad DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

AICIE Sponsored FDP on DSP Application to Industrial Drives.

Electrical & Electronics Engineering Department has organized a Two Week FDP on "DSP Application to Industrial Drives" dated 18-11-2013 to 30-11-2013 by Dr. K Prakash and P Sadanandam for Faculty of the college and other AICTE approved colleges. Dr. V. T. Somasekhar has given a lecture series and demonstration on various DSP applications to Industrial Drives. No of Participants Faculty Participants: 64

The programme is started with welcome note by the Principal **Dr. Ch. Sathaiah** followed by the lecture session.

BRIEF REPORT ON AICTE SPONSORED FACULTY DEVELOPMENT PROGRAMME

"DSP APPLICATIONS TO INDUSTRIAL DRIVES"

Introduction

Power Electronic Devices are finding its applications in permanental and industrial distribution system. If Customized with DSP power devices are used in applications such as voltage control, speed control, nower quality improvement and distributed power generation. Recent advances in power electronic conveniers with space vector modulation, multi-phase techniques, multi-pulse techniques of output control has led to reduced filter requirement and DSP control techniques. These advances have found power electronics applications in FACTS devices used under high voltage system, smart grid, distributed generation atta

Power Electronic converters are norlinser in nature and cause many power quality problems. In addition they are sensitive to power quality disturbances which lead to mai-operation or damage of the sensitive equipment. Power quality disturbances such as voltage sag, swell, harmonics, transients and noise are to be monitored and suitable mitigation action to be taken. Advances in power electronic convertors enhanced with DSP leads to intelligent design, modeling and analysis of various applications which are in progress which result into less harmonic currents and desirable power factor operation.

Objective of the Programma

The main objective of this FDF is to introduce recent developments in Digital signal processing (DSF) and its applications in the power electronic converters, motor drives with emphasis on DSP, application of space vector based PWM Techniques imultilevel inveners, Direct torque control and space based electric drives, harmonic cancellation, converters (AC/DC.DC/DC.DC/AC.AC/AC Prequency changer), power electronic data sequestion their topologies and control techniques, multi-phase inverter, PWM-controlled rectifiers, Converters used in distributed generation and custom power devices used for power quality. improvement. Several case studies presented in this FDP helped the faculty to gain in depth knowledge with emphasis to research. Laboratory sessions are arranged to provide hands on experience. This FDP aims to give exposure to possible research in this area with emphasis on Research, with case studies and technical information. This gathering with multidimensional approach is the great opportunity for faculty to share their proficiency and promote their research and industrial skills. Resource persons being from IITs, NITs and other premier institutions contributed for this objective

Programme Schedule

The programme DSP APPLICATIONS TO INDUSTRIAL DRIVES' storing 16 November to 16 November 2011 is designed in such a way that, the participant starts with concept of Power-Electronics Conveners & Digital signal processing, their advances and control algorithms for output control and changes in convenier topologies to improve efficiency. Introduction of Digital signal processing lab sessions both simulation and practical in power electronics and Digital signal processing laboratory dealing with mitigation of several power quality disturbances were also covered during this 2-week FDP. The detailed Programme Schedule is enclosed in Annexure-I.

Execution of the Programme

With the above objective in mind, EEE Department of Vaagdevi College of Engineering has applied to All India Council for Technical Education (AICTE) under Faculty Development Programme Scheme by the theme "DSP APPLICATIONS TO INDUSTRIAL DRIVES" during 18th November to 30th November-2013. AICTE has approved the Programme and sanctioned an amount of Rs. 4, 00, 000/- (Rupees Four Lakhs only) in the month of November, 2013. Details of Faculty Development Programme in the form of Brochure were communicated in the second week of october 2013 to 500 Engineering Colleges throughout the country, out of which 80 applicants responded within the stipulated time (13-11-2013) of which sixty four (64) participants were selected and confirmed by E-mail and Telephone. Details of Participants are enclosed in Annexure-II.

Resource Persons

The resource persons with Digital signal processing, Electrical Power System, Power Electronics and relevant specializations are mainly drawn from different institutions and industry like IIT Madras, IIT Hyderabad, NIT Warangal, INTU Hyderabad, INTU Jagityala, Osmania University Hyderabad, apart from internal faculty to meet the theme "DSP APPLICATIONS TO INDUSTRIAL DRIVES".

Dr V.T. Somasekhar, Professor, Dept. of EE, NIT Warangal delivered the keynote address on Inaugural session. The detailed schedule of resource persons and their topics presented date wise are attached in ennexure-1

Course Material Details

The course material is prepared in two volumes. The first one "DSP APPLICATIONS TO INDUSTRIAL DRIVES" is exclusively prepared for familiarizing the participants in the Fundamentals of Power Electronics Converters such as Rectifier, Inverter, Dc-Dc converters, PWM techniques, Introduction of applications and scope of DSP to Distributed generation such as solar, wind and small hydro generation with applications of power electronics in these areas, Power quality issues – Monitoring, ANN techniques, Fuzzy control techniques, Effects of Power quality problems and mitigation methods. Also theory and step by step procedure of software tools such as PSCAD, MATLAB-Simulink and MiPower are introduced. It is published within a DVD were handed over on valedictory function. The second one contains the lecture notes of the invited outstation resource persons and in house resource persons. It is published in soft bound fashion. The above two modules along with 100 page long note book with pen and the entire program schedule and list of participants with address details, photo gallery, resource persons details, were handed over in an executive bag to the participants on the inaugural day i.e. 18th November, 2013.



DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING VAAGDEVI COLLEGE OF ENGINEERING, WARANGAL AICTE Sponsored Faculty Development Programme DSP APPLICATIONS TO INDUSTRIAL DRIVES (18th November to 30th November, 2013)



AGENDA

- 1. Welcome by Mr.T. Venu Gopal, Asst. Prof., EEE.
- 2. Jyothi Prajwalana
- 3.Invocation
- 4. Opening Remarks by President Prof. Ch. Sathaiah, Principal, VCEW.
- 5. Report by Dr. K. Prakash, Coordinator
- Address by Chief Administrative Officer Sri.C.Satyapal Reddy, Vaagdevi Group of Technical Institutions.
- 7.A) Introduction of Chief Guest by Mr. N. Mahender, Asst. Prof., EEE.
 - B) Address by Chief guest, Dr. V.T.Somasekhar , NIT, Warangal.
- 8. Felicitation to chief guest Dr.V.T.Somasekhar, NIT, Warangal.
- 9. Vote of thanks by Mr. P. Sadanandam, Co-coordinator.



Inaugural and Lecture Sessions

- FDP started with an inauguration on 18th November, 2013 with Dr. V.T. Somasekhar, Prof., NIT, Warangal as the chief guest. Dr. Ch.Sathalah, Principal, VCEW and Dr. K.Prakash, Coordinator of this FDP and HOD, EEE Dept. were present during the inauguration. Faculty of EEE Dept. and Heads of other departments were present.
- On 18th November Dr.P.Sriaivas Osmania University Hyderabad has given lecture on Modeling and control of Switched Reluctance Drive.
- On 18th November Mr.E.Vidya Sagar, Osmania University Hyderabad has given lecture on Enhancing Integration of Renewable in Distribution Systems through Smart Links.
- On 19th November Prof. V.T. Somasekhar of NIT Warangal who was the Chief Guest for the Inaugural function has delivered lecture on Implementation of SVPWM Technique with 320LF2407A Processor.
- On 19th November Prof. V.T. Somusekhar of NIT Warangal who was the Chief Guest for the Inaugural function has delivered lecture on Multilevel Topologies and Open end winding IM Drives.
- On 19th November Prof. V.T. Somasekhar of NIT Warangal who was the Chief Guest for the Inaugural function has delivered lecture on Elimination of zero sequence currents in open end winding IM Drives.
- On 19th November Prof. V.T. Somasekhar of NIT Warangal who was the Chief Guest for the Inaugural function has delivered lecture on Vector control of IM with new 3-level Inverter.
- On 20th November field trip Was organized to 400Kv unmanned substation of power grid corporation of India.Ltd permission letter enclosed in Annexure -V
- On 21st November, Dr.S. Srinivas, of 1IT, Madras has given lecture on PWM methods, implementation and implications
- On 21st November, Dr.S.Srinivas, of IIT, Madras has given lecture on Multilevel Inverter and multilevel PWM methods.
- On 21st November, Dr.S.Srinivas, of IIT, Madras Lab session.
- On 22nd November Dr. K. Nagasujatha, JNTUCEH, Jagityala has presented a session on Direct Torque Control of Induction Motor Drives.
- On 22rd November Dr. K. Nagasujatha, JNTUCEH, Jagityala has presented a session on Adaptive controllers.
- On 22nd November Mr.K.Srinivas, JNTUCEH, Jagityala has presented a session on Design and implementation of Active Power Filter for Power Quality improvement.
- On 23rd November Dr.M.Sydulu, NIT, Warangall has presented a session on Research Methodologies.
- On 23rd & 24^{rh} November Dr.K.Shiva Kumar 11T, Hyderabad has presented a session on Architecture of DSP TMS 320F2407, Important Peripherals of DSP Processor, Cascaded multi modular multilevel inverters (review), Development of multilevel inverter from basic blocks, Implementation of Asynchronous PWM technique for multilevel inverters using TMS 320F2407/2812.

- On 25th & 26th November Dr.N. Vishwanathan, NIT, Warangal has presented a session on Resonant converters, Phase Modulated Converters, Induction Heating-I, Induction Heating-II, Tri Dimentional power converters, Solar Power connected to Grid, HVDC Power Supplies, LED Lighting.
- On 27th November Dr.S.Srinivasa Rao, NIT, Warangal has presented a session on Maximum Power Tracking Techniques for Wind Energy Systems, Research Trends in Solar Photovoltaic Technology, Research Trends in Chemical Sources of Energy (Fuel Cells), Signal Processing Techniques, Power System Harmonics and Mitigations
- On 28th November, Dr. Srinivas Vatsal, Rt. Scientist, DRDO, New Delhi has presented a session on Kalman filter on Power Electronics and Power System Applications.
- On 28th November, Dr.K.Prakash, VCE, Applications to Metauristic Techniques.

Some Important Photographs of inauguration and rest of the program are enclosed in Annexure-III.

Press clippings are enclosed in Annexure-IV

Valedictory Function was arranged on 30th November afternoon. Certificates and FDP Sessions CDs were issued to participants.

Valedictory function and feedback oral and written were collected sample format enclosed in Annexure-V

Receipts for the DD's from the participants were collected and acknowledgement of receipts given sample format enclosed in Annexure -V

Certificate of participation were distributed by the chief guest enclosed in Annexure-V

Boarding and Lodging Arrangements to Participants

All the participants were accommodated in Star lodge at Hanamkonda. Double rooms were provided for all participants. Lunch arranged for all resource persons, supporting staff along with participants in the dining hall. Dinner arranged only for participants in the hotel itself.

Valedictory Session

Valedictory session was organized on the last day of the programme i.e. 30-11 - 2013 in the Vaagdevi College of Engineering (VCE) Auditoriumin A- block. Dr. S. Vathsal, Retired Scientist, DRDO was the Chief Guest of the function. Prof. K. Kishan Rao, Director, Vaagdevi Colleges addressed the gathering. Dr. K. Prakash, coordinator spoke on this occasion. Some of the participants responded with their feedbacks highlighting the pros and cons of the programme. All feedbacks were found to be satisfactory. A sample feedback form is attached in Annexure-V. Certificates to the participants were distributed by Dr. S. Vathsal and Prof. Ch. Sathaiah, President of Valedictory function Participation certificate distributed to the participants enclosed in Annexure-VI. The programme was concluded with Vote of Thanks by Sri. P. Sadanardam, Co-Cordinator, Associate Professor, VCEW.

ANNEXURE. I Program Schedie AICTE SPONSORED FACULTY DEVELOPMENT PROGRAMME SCHEDULE DEP APPLICATIONS TO INDUSTRIAL DEI PENIS" Nov. 10 SF" Nov. 2013 WEEK - 1

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Date	Monday)	(Tuesday)	(Vetassai)	21/11/2013 (Thurscay)	22/11/2013 (Friday)	23/11/2013 (Saturday)	(Sueday)
09.30AM to :1.09AM	longeral session	Implementation of SVPWN Technique with 130LE2407A Technique with 130LE	Fuver-Ord Cosp Puver-Ord Cosp #JUK v unnanad Substition	imperioration & impleadors (Dr S. Schwas, IIT Madras)	Direct orque Control of Incustion Motor Drives (Ur-K.Naga Sujatha JNITUCE, Jagietya))	Besearch Methodologist (Dr.M.Sydulu, NTI, Wigi)	Development of multi-evel inverter from basis blocks (IbrK.S.vs.K.uran IT., (lyderabac)
ILLIGAM 83 IMANGII			ĸ	BREAL	TEA		
11.1UAM ti 12.48PM	Modeling and control of Savialized Reductance Drive (Dr. P.S. initias O.U., f(yd)	Multikve: Topologics and Open end winding IM Drives (Or. v. T. Screashcker NIT., Wgf)	Field Trip Power-Grid,400Kv unmanned Substation	Adultibase Investor and neurifiered PWM methods (De S.Sfigira), IIT, Madras)	Acaptive convolers (Dr. K. Naga Sajatha JNTUCE, Jagithya))	Architecture of ITSP TMS 320F2407 (Dr.K.Siva Kunner IT. Hyderabad)	Development of multilevel invester from basic blocks (Dr.K. Siva Kumar, IIT., Hydenabad)
12.40PM			-	2 4	= 0		7
1.30FM to 3.60FM	Violeting and control of Switched Reluctance Drive (Dr.P.Srinivas G.U. Hyd)	Elimination of zero sequence currents in open and winding IM Drives (Dr.Y.T.Somachekat, NIT, Wgl)	Field Trip Pawer Grid Corp 400Kv unmerated Substation	(Dr.S.Sirilyas, IT Madres	Design and implementation of Archive Fourier Filter for Fourier Quality improvement (Nd. K. Stitirous, FATUCE, fugitively	Important Periphesals of DSP Processor (Dr.K.Siva Kumar 11. Hyderatud)	Implementation of Asynchronous PWM restrictives for multi-over invertees using TMS 34CF-457/2812 (Ur. A. New Kumar, III., Hefermone)
3 10 PM	•		к	BREA	TEA		
5,000	Entereing Integration of Ranawathie in Distribution Systems through Smart Links (Mr.E. Vidye Sages, OLLFyd)	Vector control of IM with new 3-level Inventor (Dr.V.) Sernashelar N.T. Wgl	Peter Trip Power-Drift Corp 400Kv ususassed Sabsation	(Dr.S.Scarvas, IIT Medicas)	Design and implementation of Active Power Filter for Power Quality Improvement (Mer.Keinivan, JNTUCS). Jagethyati	Cascaced mails modular multilevel invertes (review) (Dr.K.Siva Kumar IIT. Hydemhad)	Ingletentation of Asynchronous PWM technique for multilevel inverters using TMS 32(F2-07/2412 (Dr.K.S.vi. Kamar, III. Hydenskel)

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Tiesings/ Date	25/11/2013 (Menday)	26/11/2013 (Tuesday)	27/11/2013 (Wednesday)	28/11/2013 (Thursday)	19/11/2013 (Friday)	30/11/2013 (Saturday)
99.30 AM to 11.00 AM	Resonant converters (LY-N-Vishwasathan NIT, Wg/)	Tri Dimentional power converters (Er.N. Vishwanathan NIT, Wg)	Maximum Power Fracking Fechniques for Wind Linergy Systems (Dr.S.Sitniwas Fao, NIT, Wg.)	Karmen filter on Power Executorics and Power System Applications (Dr. Striates Vassal RLSvienda, DR.DO, New Delia)	4-Love Invester for Open End winding Induction Moror Deve (Dr.B. Venughpid Reddy NIT, God)	Problem Formulation Dr. Srinivasan Vatsal
11.10AM			BREAK	TEA	3	
11.16AM to 12.48PM	Phase Modulated Convertors (Dr.N.Vishwansthan MT.Wg0	Solar Power connected to Geld. (Dr.N. Visywa sathan NIT, Wg)	I.Research Trends in Solar Rodovolitic Technology 2.Research Trends in Chemical Sources of Energy (Fud Colls) (Dr S.Jelrivan Kan, htt; wg)	Kaliran filler on Power Electronics and Fower System Applications (Dr. Shinires Vetsel RLSeienist, DRDO, New Delhi)	t-Level Invertor for Open End winding Induction Motor Drive (Dr.B.Venugopal Reddy NIT, Goa)	Research Experiences Dr. Srinivasan Vatsal
12.40FM			zer	= 0		
1.30FM to 3.00FM	Induction Hearing-I (Dr.N. Vishwarastan NIT, Wgl)	HVDCPower Supplies [D-,NVshwaradian NIT,Wgl)	Signal Processing Techniques (Dr.D.V.S.S.Sva Sarna NIT, Wgb	Applications to Metamistic Techniques (D.:K.Prakash, VCE, Vg.)	Lab Session (Dr.B. Nenugopal Reddy, NIT. Gos. & Mr.P. Sadansadam, VCE, Wgl)	Research Experiences Dr. Srinivasan Vatsal
310 PM			BREAK	TEA		
Section on section	foduction Heating -(I (Dr.N.Vishwanathan NIT,Wg)	LEO Lighting (Dr.N. Vishwanashan NIT, Wal)	Pewer System Harmorites and Mitigations (Dr.D.v.S.S.Siva Samin NIT, Wgl)	Modeling and Simulation of Power Electronic Convertor (Dr.D Venegopal Reddy NIT, Con)	Lab Sesson (Dr.B. Venugopal Reddy NIT, Goa & Mr.P. Sadanandam, VCE, Wgl)	Valedictory

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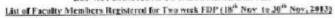
WEEK-2

ANNEXURE-II



DEPARTMENT OF ÉLECTRICAL AND ELECTRONICS ENGINEERING VAAGDE VI COLLEGE OF ENGINEERING, WARANGAL

AICTE Sponsored Faculty Development Programme DSP APPLICATIONS TO INDUSTRIAL DRIVES





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	47	Mr.K.Gogoda Krishea	Asst Pool	Bulay Incitate Engineering and Sciences, Nationspot	9173063800	gapalalorellera konvelingmad one

Annexure-III

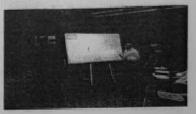
Important Photographs



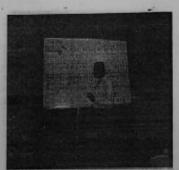
Dr. V.T. Sommerschar, NIT Warrangal Chief Guest Inaugural Function of the FDP



18-11-2013 Mr.E. Vidyasagar , Osmania university Clg. of Eagg.



19-21-2013 Dr. V.T. Somasekhar, NIT Warangal



19-11 2013 Dr. V.T. Som aselchar, NIT Warangal



26-11-2013 Field trip to Power Grid Corporation



21-11-2013 Dr.S.Srinivas, IIT, Madras



22-12-2013 Dr.K.Nagasujatha, JNTUCEH, Jagityal



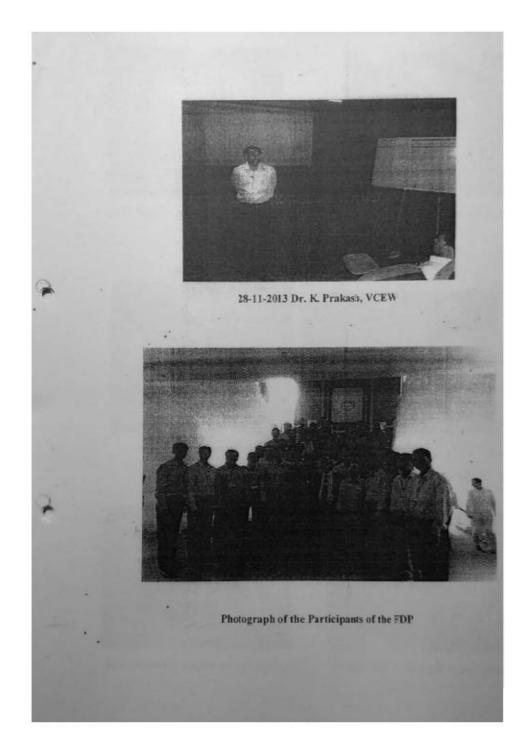
23-11-2013 Dr.K.Siva Kumar, HT, Hyderabad



25&26-11-2013 Dr. N. Vishwanathan, NIT Warangal



26-11-2013 Dr.S.Vathsal Former Scientist



Annexure-IV

Press Clippings in Local News Papers

సాంకేతిక ప్రచారం వైపు దృష్టి సాలంచాల

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Andhrajyothi 19-11-2013

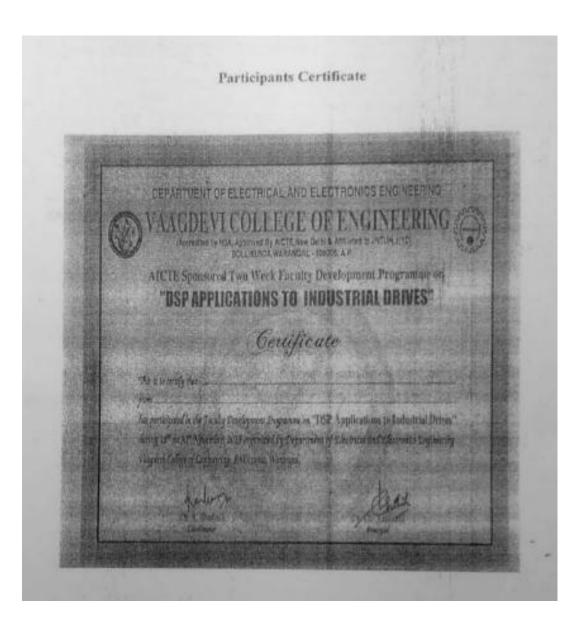
శాస్త్రీయ పలిశోధనలతోనే సమాజాభవృద్ధి

టాఖ్లకుంట(నంగెం), స్యూస్స్టిస్ : శాస్త్రీయ వరిళోధనలతోనే సమాజాభివృద్ధి సాధ్య మని, ఇందుకు ప్రభుత్వంతోపాటు విద్యానం మన, మదువ అడుత్వంల పోటు చెల్లాను స్టలు ముఖ్యప్పేత పోషించాలని రిట్రిక్ శాస్త్ర వేశ్ర మ్యాడిస్టీ డీఆర్డ్ ఓ సంచాలకులు ద్యార్ శ్రీనివానన్ చక్కల్ అన్నారు. మండలంలోని పోస్టకులు వార్డెవి ఇంజనీరింగ్ కళాశాలలో చెప్పిందు వార్డెవి ఇంజనీరింగ్ కళాశాలలో చెప్పిందు పోజాలుగా అధ్యాభకులు పాంక ఈ శ్రక్షాణా శిభినం డీఎస్స్ అస్టికేషన్స్ బు జండట్రియర్ డైప్స్ అన్ అంగంపై నిర్వహ స్తృన్నారు. వ్యవారం శిక్షలా శివీరంలో జందట్టియల్ డైమ్స్ అన్ అంగంపై నిర్వహ్ స్ట్రహ్మాడు. ఇట్టివారం ఇడ్లవా శేవీరంలో మాట్లాముత్తున్న ఓంటాననే ఇత్కలే ముఖుల90గా విచ్చేసిన ఆయన మాట్లా మగయగంలో ఓస్స్ లప్లేకప్పుకు ఎంతో ప్రాటెక్కు క్రాణ్ అరారంగా దీస్పూ దుళ్ళా మస్తుల విద్యావిధానంలో సమూం పూర్పాలు ఆనకరముని, రేగ్ కాతం టోధన్ని, నుమ్మాడిక్స్ ప్రాటెక్స్ క్రాణ్ స్ట్రాటిక్స్ స్ట్రాట్స్ స్ట్రాటిక్స్ స్ట్రాటిక్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాటిక్స్ స్ట్రాట్స్ స్ట్ స్ట్రాట్స్ స్ట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రార్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రార్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రాస్ట్ స్ట్రాట్స్ స్ట్రాట్స్ స్ట్రార్స్ స్ట్రార్స్ స్ట్రార్స్ స్ట్స్ స్ట్రార్స్ స్ట్ స్ట్రార్స్ స్ట్రార్స్ స్ట్రార్స్ స్ట్స్ స్ట్స్ పొధించవక్కన్ కెప్పారు. డాగ్రేవ్ విద్యానం రైల కైరెక్టర్ కె.కిమర్గంపై మాట్లాడుతూ అడు



ుంతో వాడల పెటిందల్నారు. సైప్పెఫాల్ కానందం. అడ్డిన్ సైషన్ డిక్ ఓనెంకటేరిడ్డి. నత్తన్నా మాట్లాడుతూ ఎంటీ, ఎసీఎల్ వివిధ కావానిచేతులు పాల్చిన్నారు.

Eenadu 30-11-2013



Head of the Department

Head of the Department

ELECTRICAL & ELECTRONICS ENGINEERING

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