
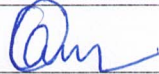

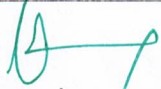
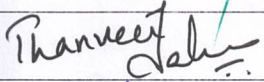



**VAAGDEVI COLLEGE OF ENGINEERING**  
**(Autonomous)**


**DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING**  
**BOARD OF STUDIES MEETING**

Minutes of meeting of Board of studies in Computer Science and Engineering held on 28-1-2021 at 10:30am.

**Members Present:**

S.No	Name and Address	Designation	Signature
1	Dr. V. Janaki Professor & Head, CSE, VCE.	Chairman	
2	Dr.D.Vasumathi Professor, CSE-JNTUH CEH.	Member (Univ. Nominee)	
3	Prof. R.B.V. Subramaanyam Professor, NITW.	Member (Subject Expert)	
4	Dr. V.B.Narsimha Assistant Professor, CSE,UCE, Osmania University, Hyderabad.	Member (Subject Expert)	
5	Dr.Thanveer Jahan Associate Professor, CSE,VCE.	Member	
6	Dr. M.Sukesh Assistant Professor, CSE, VCE.	Member	
7	Mr. B. Aravind Software Engineer, IBM.	Member (Alumni Member)	
8	Mr. Sarath. J Director, Innominds India pvt. ltd.	Member (Industry Representative)	

1. The course structure of B. Tech (R20) regulations for III and IV semesters with the following changes in R18. Proper care is taken to see that AICTE / JNTUH norms / regulations are satisfied.
  - a. To take care of lateral entry students, "English for effective communication skills" Theory subject is moved to III semester, retaining the credits as it is.
  - b. Project Based Learning (PBL) is introduced in III and IV semesters with 1 credit.
2. Approved the scheme and syllabi of B.Tech CSE (R20-Regulations) for III and IV Semesters.

  
CHAIRMAN  
Board of studies

**VAAGDEVI COLLEGE OF ENGINEERING  
(AUTONOMOUS)  
COMPUTER SCIENCE AND ENGINEERING  
COURSE STRUCTURE  
(2020 - 2021)**

**III - SEMESTER**

S. No	Course Code	Title of the Course	L	T	P	Credits
1		Design and Analysis of Algorithms	3	0	0	3
2		Digital Logic Design & Micro Processors	3	0	0	3
3		Mathematical Foundations of Computer Science	3	0	0	3
4		Java Programming	3	0	0	3
5		English For Effective Communications	2	0	0	2
6		Digital Logic Design & Micro Processors Lab	0	0	3	1.5
7		Design and Analysis of Algorithms Lab	0	0	3	1.5
8		Java Programming Lab	0	0	3	1.5
9		Project Based Learning- 1	0	0	2	1
<b>Total Credits</b>						<b>19.5</b>

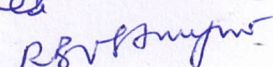
**IV - SEMESTER**

S. No	Course Code	Title of the Course	L	T	P	Credits
1		Operating Systems	3	0	0	3
2		Formal Languages and Automata Theory	3	0	0	3
3		Computer Organization & Architecture	3	0	0	3
4		Database Management Systems	3	0	0	3
5		Probability and Statistics	3	0	0	3
6		Database Management Systems Lab	0	0	3	1.5
7		Operating Systems Lab	0	0	3	1.5
8		Web Technologies Lab	0	0	3	1.5
9		Project Based Learning - 2	0	0	2	1
<b>Total Credits</b>						<b>20.5</b>

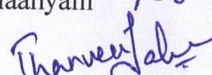
1. Dr. V. Janaki



3. Prof. R.B.V. Subramaanyam



5. Dr. Thanveer Jahan



7. Mr. B. Aravind

2. Dr. D. Vasumathi



4. Dr. V. B. Narsimha



6. Dr. M. Sukesh



8. Mr. Sarath. J