DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the meeting of the Board of Studies held on 5|2|22

The following members were present:

SL.No	Members Present	Designation	Signature
1.	Dr. M. Shashidhar Assoc. Prof., ECE Dept., VCE Warangal.	Chairman	
2.	Mrs. T. Madhavi Kumari, Assoc. Prof. & Addl. Controller of Examination, JNTU, Hyderabad.	JNTUH Nominee	5/2/22
3.	Dr. Y. Raghavendra Rao, Head of the Department JNTU , Sulthanpur	Subject Expert	
4.	Prof. P. Prasad Rao, Principal, VEC Warangal	Subject Expert	
5.	Mr. P. Rohit, CEO, Suhrud Applab Private limited, Warangal	Industry Representative	
6.	Mr. Bala Krishna Islavath, Scientist, R&D, Laboratory Center for Electromagnetic, Ministry of Electronics and Information Technology, Government of India	Undergraduate Alumni	
7.	Mr. P. Mahesh Goud, Senior Silicon Design Engineer at AMD	Undergraduate Alumni	
8.	Dr. Jitesh Ramdas Shinde, Asst. Prof, ECE Dept, VCE, Warangal	Member	
9.	Dr. M. Ranjeeth, Asst. Prof, ECE Dept, VCE, Warangal	Member	
10.	Dr. Hemant Kumar Gupta, Asst. Prof. ECE Dept, VCE Warangal	Member	

The following decisions are taken:

1. The course structure and syllabi of B.Tech Electronics and Communication Engineering V, VI, VII & VIII semesters of R20 regulation are finalized and approved.
2. New courses: (i) IoT (ii) Robotics and Automation. (Iii) Artificial Intelligence (iv) Machine Learning are introduced in R20 curriculum.
3. The BOS members suggested to include Design of Fault Tolerant Systems subject to enhance the knowledge in digital system and circuits testing in R20 curriculum.
4. B.Tech Internet of Things (IoT) in Minor programme Course Structure and syllabus are finalized and approved.

(Chairman-BOS)

CHAIRMAN
Board of Studies, ECE Dept.,
VAAGDEVI COLLEGE OF ENGINEERING
Bollikunta, Warangal (T.S), ECE.

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Autonomous
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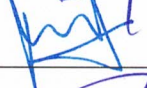## B.TECH. ELECTRONICS & COMMUNICATION ENGINEERING
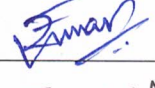## COURSE STRUCTURE
(R20 Regulations applicable for the batches admitted from Academic Year 2020-21)

V SEMESTER

S.No.	Subject code	Subject	L	T	P	Credits
1		Linear & Digital IC Applications	3	0	0	3
2		Digital Signal Processing	3	0	0	3
3		Control Systems	3	0	0	3
4		**Professional Elective-I** Electronic Measurements and Instrumentation Computer Networks Basic JAVA Programming	3	0	0	3
5		Managerial Economics & Financial Analysis	3	0	0	3
6		Linear & Digital IC Applications Lab	0	0	2	1
7		Digital Signal Processing Lab	0	0	2	1
8		Advanced English Communication skills lab	0	0	2	1
9		Project Based Learning-3	0	0	2	1
		Total Credits	**15**	**0**	**8**	**19**

VI SEMESTER

S.No.	Subject code	Subject	L	T	P	Credits
1		Microprocessors & Microcontrollers	3	0	0	3
2		VLSI Design	3	0	0	3
3		Antennas & Wave Propagation	3	0	0	3
4		**Professional Elective-II** Design of Fault Tolerant Systems Fiber Optical Communications Digital Image Processing	3	0	0	3
5		**Professional Elective-III** Radar Systems Speech Processing Machine learning	3	0	0	3
6		VLSI & e-CAD Lab	0	0	3	1.5
7		Microprocessors & Microcontrollers Lab	0	0	3	1.5
8		Project Based Learning-4	0	0	2	1
9		Logical Reasoning & Quantative Aptitude	0	0	2	0
		Total Credits	**15**	**0**	**10**	**19**

Dr. M. Shashidhar		Mr. Bala Krishna Islavath	
Mrs. T. Madhavi Kumari		Mr. P.Mahesh Goud	
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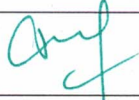## B.TECH. ELECTRONICS & COMMUNICATION ENGINEERING
COURSE STRUCTURE
(R20 Regulations applicable for the batches admitted from Academic Year 2020-21)

VII SEMESTER

S.No.	Subject code	Subject	L	T	P	Credits
1		Microwave Engineering	3	0	0	3
2		Embedded Systems	3	0	0	3
3		**Professional Elective-IV** Wireless and Mobile Communication CMOS Circuit Design Artificial Intelligence	3	0	0	3
4		**Professional Elective-V** Sensor Networks Satellite Communication Robotics and Automation	3	0	0	3
5		**Open Elective – I**	3	0	0	3
6		Microwave Engineering Lab	0	0	2	1
7		Embedded Systems Lab	0	0	2	1
8		Mini Project and Internship	0	0	4	2
9		Project Phase – I	0	0	8	4
		Total Credits	15	0	16	23

VIII SEMESTER

S.No.	Subject code	Subject	L	T	P	Credits
1		**Professional Elective-VI** Digital Signal Processor & Architecture FPGA Architecture & Applications Internet of Things	3	0	0	3
2		**Open Elective – II**	3	0	0	3
3		**Open Elective – III**	3	0	0	3
4		Technical Seminar	0	0	2	1
5		Project Phase – II	0	0	16	8
		Total Credits	9	0	18	18

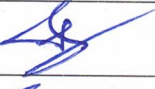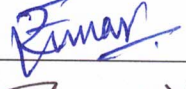|			
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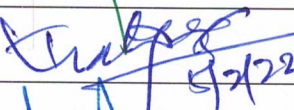
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B.TECH. ELECTRONICS & COMMUNICATION ENGINEERING

B.Tech. Minor in Internet of Things Course Structure

(Applicable for R18 & R20 Regulations)

B.Tech. Minor in Internet of Things III year I semester syllabus

Year/Semester	Theory (3 Credits, 3 Hours)	Laboratory (3 Credits, 3 Hours)	Total Credits
III - I Semester	Advanced Python Programming	Advanced Python Programming Laboratory	4.5
III - II Semester	1. Introduction to IoT 2. Smart Technologies	--	6
IV - I Semester	Programming Languages for IoT	IoT Automation with Raspberry-PI Laboratory	4.5
IV - II Semester	Fog & Edge Computing for IoT	--	3
		Total Credits	**18**

Dr. M. Shashidhar		Mr. Bala Krishna Islavath	
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