



VAAGDEVI COLLEGE OF ENGINEERING

Autonomous

Bollikunta, Khila Warangal (Mandal), Warangal Urban-506 005 (T.S), www.vaagdevi.edu.in

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

Minutes of the meeting of the Board of Studies held on: 12/2/21

The following members were present:

SL.No	Members Present	Designation	Signature
1.	Mr. M. Shashidhar Assoc. Prof & HOD., ECE Dept., VCE Warangal. sasi47004@gmail.com	Chairman	
2.	Mrs. T. Madhavi Kumari, Assoc. Prof. & Addl. Controller of Examination, JNTU, Hyderabad. thoomati@jntuh.ac.in, thoomati@gmail.com	JNTUH Nominee	 12/02/2021
3.	Dr. Y. Raghavendra Rao, Head of the Department JNTU, Sulthanpur	Subject Expert	
4.	Prof. P. Prasad Rao, Principal, VEC Warangal Principal.vec@gmail.com	Subject Expert	
5.	Mr. P. Rohit, CEO, Suhrud Applab Private limited, Warangal mails2rohith@gmail.com	Industry Representative	
6.	Bala Krishna Islavath, Scientist, R&D, Laboratory Center for Electromagnetic, Ministry of Electronics and Information Technology, Government of India islavath32@@gmail.com	Undergraduate Alumni	
7.	Mr. P. Mahesh, Senior Silicon Design Engineer at AMD Goud.mahesh058@live.com	Undergraduate Alumni	
8.	Dr. Jitesh Ramdas Shinde, Asst. Prof, ECE Dept, VCE, Warangal shindejitesh.vaagdevi.coe.ece@gmail.com	Member	
9.	Dr. M. Ranjith, Asst. Prof, ECE Dept, VCE, Warangal ranjithmamidi2001@gmail.com	Member	
10.	Dr. Nishu Gupta, Assoc. Prof. ECE Dept, VCE Warangal headece@vaagdevi.edu.in	Member	

The following decisions are taken:

1. The course structure and syllabi of B.Tech Electronics and communication engineering III and IV semesters of R20 regulation are finalized and approved.
2. The following changes are made
 - i. The proposed laboratory course **Electronic Simulation Lab** is modified as **Electronic Simulation EDA Tools Lab** and the syllabus is framed according to the present scenario.
 - ii. In order to enhance practical knowledge Project based learning is introduced in III and IV semesters with 1 credit each.
 - iii. The new lab course **Hardware Design Lab** is introduced in IV semester.

(Chairman-BOS)



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III SEMESTER

S.No.	Subject code	Subject	L	T	P	Credits
1		Numerical Methods and Complex Variables	3	1	0	4
2		Signals and Systems	3	0	0	3
3		Electronic Circuits Analysis	3	0	0	3
4		Switching Theory and Logic Design	3	0	0	3
5		Electrical Technology	3	0	0	3
6		English for Effective Communication	2	0	0	2
7		Electronic Circuits Analysis Lab	0	0	3	1.5
8		Electronic Simulation EDA Tools Lab	0	0	3	1.5
9		Project Based Learning-1	0	0	2	1
10		NSS/NCC	0	0	2	0
Total Credits			17	01	10	22

IV SEMESTER

S.No.	Subject code	Subject	L	T	P	Credits
1		Pulse and Digital Circuits	3	0	0	3
2		Analog and Digital Communications	3	0	0	3
3		Electromagnetic Theory and Transmission Lines.	3	0	0	3
4		Probability Theory and Stochastic Process	3	0	0	3
5		Computer Organization.	3	0	0	3
6		Pulse and Digital Circuits Lab	0	0	3	1.5
7		Analog and Digital Communications lab	0	0	3	1.5
8		Hardware Design Lab	0	0	2	1
9		Project Based Learning-2	0	0	2	1
Total Credits			15	00	10	20

1. Mr. M. Shashidhar

2. Mrs. T. Madhavi Kumari

3. Dr. Y. Raghavendra Rao

4. Prof. P. Prasad Rao

5. Mr. P. Rohit

6. Bala Krishna Islavath

7. Mr. P. Mahesh

8. Dr. Jitesh Ramdas Shinde

9. Dr. M. Ranjith

10. Dr. Nishu Gupta