

VAAGDEVI COLLEGE OF ENGINEERING

P.O BOLLIKUNTA, WARANGAL

The following dates are allotted for conducting of External Supplementary Lab Examinations JULY-2022

II YEAR I SEMESTER

Lab Name	Regulation	H.T.Numbers				DATE
BASIC ELECTRICAL AND ELECTRONICS ENGINEERING LAB	R18/R15	17641A0122	17641A01D4	18641A01A4	17641A0366	17.08.2022
OBJECT ORIENTED PROGRAMMING THROUGH JAVA LAB	R18/R15	18645A0509				17.08.2022
DATA STRUCTURES THROUGH C++ LAB	R18/R15	20645A02B5	17641A05C6			17.08.2022
DATABASE MANAGEMENT SYSTEMS LAB	R18/R15	17641A05C6				17.08.2022
DIGITAL LOGIC DESIGN & MICRO PROCESSORS LAB	R18/R15	17641A05C6				17.08.2022
MECHANICS OF SOLIDS AND METALLURGY LAB	R18/R15	17641A0366				17.08.2022
FUELS AND LUBRICANTS LAB	R18/R15	17641A0366				17.08.2022



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III SEMESTER

Lab Name	Regulation	H.T.Numbers						DATE
STRENGTH OF MATERIALS LAB	R20	20641A0130	20641A0142					17.08.2022
SURVEYING LAB	R20	20641A0130	20641A0142					17.08.2022
ENGLISH LANGUAGE AND INTERACTIVE COMMUNICATION SKILLS LAB	R20	20641A0130	20641A0142	20641A0157				17.08.2022
PYTHON PROGRAMMING LAB	R20	20641A0243	21645A0240					17.08.2022
ELECTRICAL CIRCUITS LAB	R20	20641A0218	20641A0232	20641A0243				17.08.2022
DESIGN AND ANALYSIS OF ALGORITHMS LAB	R20	20641A05E4	20641A05G7	20641A05J6	20641A05L2	20641A05L6	20641A05L9	17.08.2022
JAVA PROGRAMMING LAB	R20	20641A05E4	20641A05G7	20641A05J6	20641A05L2	20641A05L6	20641A05L9	17.08.2022
PROJECT BASED LEARNING - 1	R20	21645A0546	21645A0547					17.08.2022
DATABASE MANAGEMENT SYSTEMSLAB	R20	20641A6699	20641A66C2	20641A66E0	20641A66E1	20641A6727	20641A6734	17.08.2022
		20641A6749						17.08.2022
ELECTRONIC CIRCUITS ANALYSIS LAB	R20	20641A0443	20641A0462	20641A0464	20641A0465	20641A0471	20641A0472	17.08.2022
		20641A0474	20641A0484	20641A0488				17.08.2022
ELECTRONIC SIMULATION EDA TOOLS LAB	R20	20641A0406	20641A0417	20641A0418	20641A0421	20641A0426	20641A0431	17.08.2022
		20641A0432	20641A0433	20641A0434	20641A0484			17.08.2022
DIGITAL LOGIC DESIGN & MICRO PROCESSORS LAB	R20	20641A05E4	20641A05F6	20641A05G7	20641A05J6	20641A05L2	20641A05L6	17.08.2022
		20641A05L9						17.08.2022
MECHANICS OF SOLIDS LAB	R20	20641A0321	20641A0322	20641A0324				17.08.2022



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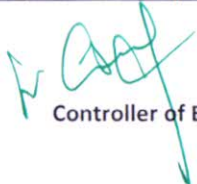
P.O BOLLIKUNTA, WARANGAL

The following dates are allotted for conducting of External Supplementary Lab Examinations JULY-2022

II YEAR II SEMESTER

Lab Name	Regulation	H.T.Numbers					DATE
SURVEYING LAB-II	R18/R15	17641A0151					18.08.2022
ENGINEERING GEOLOGY LAB	R18/R15	17641A0151					18.08.2022
ELECTRICAL MACHINES-I LAB	R18/R15	17641A0219	18641A0216	19641A0226	20645A02B5		18.08.2022
PULSE DIGITAL AND LINEAR INTEGRATED CIRCUITS LAB	R18/R15	19641A0226	20645A02B5				18.08.2022
FLUID MECHANICS AND HYDRAULIC MACHINERY LAB	R18/R15	20645A0368	18641A0305	17641A0151			18.08.2022
PRODUCTION TECHNOLOGY LAB	R18/R15	20645A0336	20645A0337	20645A0368	18641A0305		18.08.2022
OPERATING SYSTEMS LAB	R18/R15	17641A05C6					18.08.2022
WEB TECHNOLOGIES LAB	R18/R15	18641A05K4	17641A05C6				18.08.2022


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III YEAR I SEMESTER

Lab Name	Regulation	H.T.Numbers						DATE
DIGITAL COMMUNICATIONS LAB	R18/R15	14641A04M8						18.08.2022
COMPUTER NETWORKS AND COMPILER DESIGN LAB	R18/R15	18641A05A5	19641A05L1	20645A0522				18.08.2022
MACHINE LEARNING LAB	R18/R15	19641A05L1	17641A05C6					18.08.2022
COMPUTER NETWORKS LAB	R18/R15	19641A04A3	19641A04C3	19641A04C7				18.08.2022
LDIC LAB	R18/R15	19641A0413	19641A0491	19641A0498	19641A04A3	19641A04C3	19641A04C7	18.08.2022
	R18/R15	19641A04D6	20645A0407	20645A0413	20645A0418			18.08.2022
DIGITAL SIGNAL PROCESSING LAB	R18/R15	18641A04H6	19641A0466	19641A0498	19641A04A0	19641A04A3	19641A04C3	18.08.2022
	R18/R15	19641A04C7						18.08.2022
ELECTRICAL MACHINES LAB – II	R18/R15	16641A0237	17641A0291	18641A0257	18641A0258	20645A0206	20645A0278	18.08.2022
ELECTRICAL MEASUREMENTS AND INSTRUMENTATION LAB	R18/R15	16641A0237	20645A0206					18.08.2022
CONTROL SYSTEMS LAB	R18/R15	16641A0237	17641A0291	19641A0224	19641A0232	20645A0206		18.08.2022
THERMAL ENGINEERING LAB	R18/R15	17641A0369	18641A0308	18641A0305				18.08.2022
METROLOGY AND MACHINE TOOLS LAB	R18/R15	17641A0369	18641A0308	18641A0305				18.08.2022


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III YEAR II SEMESTER

Lab Name	Regulation	H.T.Numbers					DATE
ADVANCED ENGLISH LANGUAGE AND COMMUNICATION SKILLS LAB	R18/R15	16641A0225	16641A0566	17641A04B6	18641A0305	17.08.2022	
CONTROL SYSTEMS LAB	R18/R15	16641A0225				17.08.2022	
POWER ELECTRONICS AND DRIVES LAB	R18/R15	16641A0225				17.08.2022	
POWER ELECTRONICS LAB	R18/R15	17641A0219				17.08.2022	
TECHNICAL COMMUNICATION SKILLS LAB	R18/R15	14641A04M8				17.08.2022	
MICROPROCESSORS & MICROCONTROLLERS LAB	R18/R15	14641A04M8				17.08.2022	
DIGITAL SIGNAL PROCESSING LAB	R18/R15	14641A04M8				17.08.2022	
VLSI & E-CAD LAB	R18/R15	17641A04B6				17.08.2022	
NETWORK PROGRAMMING LAB	R18/R15	16641A0566				17.08.2022	
HEAT TRANSFER LAB	R18/R15	18641A0305				17.08.2022	

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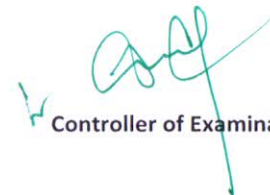
The following dates are allotted for conducting of External Supplementary Lab Examinations JULY-2022

IV YEAR I SEMESTER

Lab Name	Regulation	H.T.Numbers					DATE
ENVIRONMENTAL ENGINEERING LAB	R18/R15	16641A0155	17641A0188	17641A01D4			16.08.2022
HIGHWAY ENGINEERING LAB	R18/R15	16641A0155					16.08.2022
STRUCTURAL DRAFTING LAB OPEN SOURCE SOFTWARE	R18/R15	16641A0155					16.08.2022
VLSI & E-CAD LAB	R18/R15	14641A04M8					16.08.2022
MICROWAVE ENGINEERING LAB	R18/R15	17641A0486	17641A04B6	18641A0495	18641A04E5	19645A0402	16.08.2022
EMBEDDED SYSTEMS DESIGN LAB	R18/R15	17641A04B6	18641A0495	18641A04E5	19645A0402		16.08.2022
NETWORK SECURITY & CRYPTOGRAPHY LAB	R18/R15	17641A0581	17641A05A7	17641A05C6	18641A0541		16.08.2022
MICROPROCESSORS AND MICROCONTROLLERS LAB	R18/R15	16641A0237	17641A0201	17641A0219			16.08.2022
ELECTRICAL SIMULATION LAB	R18/R15	16641A0237					16.08.2022
ADVANCED ENGLISH COMMUNICATION SKILLS LAB	R18/R15	16641A0237	17641A0219				16.08.2022



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IV YEAR II SEMESTER

Lab Name	Regulation	H.T.Numbers				DATE
A9444 - SEMINAR	R14	14641A04M8	.	.	.	16.08.2022



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