



EXAMINATION BRANCH
VAAGDEVI COLLEGE OF ENGINEERING
(AUTONOMOUS)



Bollikunta, Khila Warangal (Mandal), Warangal Urban-506 005 (T.S), www.vaagdevi.edu.in

TIME TABLE FOR I M.TECH II – SEMESTER (R20) SUPPLEMENTARY EXAMINATIONS OCTOBER 2022

TIME: 10.00 AM - 01.00 PM

Date: 30.09.2022

BRANCH	DATE/DAY					
	13.10.2022 THURSDAY	15.10.2022 SATURDAY	18.10.2022 TUESDAY	20.10.2022 THURSDAY	22.10.2022 SATURDAY	
POWER ELECTRONICS(43)	MODERN POWER ELECTRONIC CONVERTERS	POWER ELECTRONIC CONTROL OF AC DRIVES	POWER QUALITY	AI TECHNIQUES IN ELECTRICAL ENGINEERING	ENGLISH FOR RESEARCH PAPER WRITING	-----
POWER SYSTEM CONTROL & AUTOMATION (53)	POWER SYSTEM DYNAMICS	POWER SYSTEM AUTOMATION	POWER QUALITY	AI TECHNIQUES IN ELECTRICAL ENGINEERING	ENGLISH FOR RESEARCH PAPER WRITING	-----
VLSI SYSTEM DESIGN (57)	CMOS MIXED SIGNAL CIRCUIT DESIGN	VLSI DESIGN VERIFICATION AND TESTING	HIGH SPEED VLSI DESIGN	ASIC DESIGN	STRESS MANAGEMENT	-----
COMPUTER SCIENCE & ENGINEERING(58)	ADVANCED WEB PROGRAMMING	SOFT COMPUTING TECHNIQUES	MACHINE LEARNING	BLOCK CHAIN TECHNOLOGY	STRESS MANAGEMENT	-----
STRUCTURAL ENGINEERING(20)	FINITE ELEMENT ANALYSIS OF STRUCTURES	STRUCTURAL DYNAMICS	REPAIR AND REHABILITATION OF STRUCTURES	DESIGN OF PRESTRESSED CONCRETE STRUCTURES	ENGLISH FOR SCIENTIFIC COMMUNICATION	-----
THERMAL ENGINEERING(21)	ADVANCED HEAT TRANSFER	ADVANCED I.C. ENGINES	ALTERNATE FUELS	THERMAL & NUCLEAR POWER PLANTS	ENGLISH FOR RESEARCH PAPER WRITING	-----

ADDL. CONTROLLER OF EXAMINATIONS

CONTROLLER OF EXAMINATIONS

PRINCIPAL

ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY



EXAMINATION BRANCH
VAAGDEVI COLLEGE OF ENGINEERING
(AUTONOMOUS)



Bollikunta, Khila Warangal (Mandal), Warangal Urban-506 005 (T.S), www.vaagdevi.edu.in

TIME TABLE FOR I M.TECH II – SEMESTER (R18) SUPPLEMENTARY EXAMINATIONS OCTOBER 2022

TIME: 10.00 AM - 01.00 PM

Date: 30.09.2022

BRANCH	DATE/DAY					
	13.10.2022 THURSDAY	15.10.2022 SATURDAY	18.10.2022 TUESDAY	20.10.2022 THURSDAY	22.10.2022 SATURDAY	
POWER ELECTRONICS(43)	MODERN POWER ELECTRONIC CONVERTERS	POWER ELECTRONIC CONTROL OF AC DRIVES	POWER QUALITY	AI TECHNIQUES IN ELECTRICAL ENGINEERING/ DYNAMICS OF ELECTRICAL MACHINES	ENGLISH FOR RESEARCH PAPER WRITING	-----
POWER SYSTEM CONTROL & AUTOMATION (53)	POWER SYSTEM DYNAMICS	POWER SYSTEM AUTOMATION	POWER QUALITY	AI TECHNIQUES IN ELECTRICAL ENGINEERING/ EHV AC TRANSMISSION	ENGLISH FOR RESEARCH PAPER WRITING	-----
VLSI SYSTEM DESIGN (57)	CMOS MIXED SIGNAL CIRCUIT DESIGN	VLSI DESIGN VERIFICATION AND TESTING	HIGH SPEED VLSI DESIGN/LOW POWER VLSI DESIGN	ASIC DESIGN/SYSTEM ON CHIP ARCHITECTURE	STRESS MANAGEMENT	-----
WIRELESS & MOBILE COMMUNICATION(65)	ADVANCED DIGITAL SIGNAL PROCESSING	WIRELESS COMMUNICATION	ADVANCED COMMUNICATIONS AND NETWORKS	WIRELESS MIMO COMMUNICATION/ WIRELESS LANS AND PANS	STRESS MANAGEMENT	-----
COMPUTER NETWORKS & INFORMATION SECURITY(78)	NETWORK SECURITY	WIRELESS NETWORKS	SECURITY THREATS	BIOMETRICS	STRESS MANAGEMENT	-----
COMPUTER SCIENCE & ENGINEERING(58)	NETWORK PROGRAMMING	SOFT COMPUTING TECHNIQUES	DIGITAL FORENSICS	SOFTWARE PROCESS AND PROJECT MANAGEMENT/ DISTRIBUTED DATABASES	STRESS MANAGEMENT	-----
THERMAL ENGINEERING(21)	ADVANCED HEAT TRANSFER	ADVANCED I.C. ENGINES	ALTERNATE FUELS	THERMAL AND NUCLEAR POWER PLANTS	ENGLISH FOR RESEARCH PAPER WRITING	-----
STRUCTURAL ENGINEERING(20)	FINITE ELEMENT METHODS	STRUCTURAL DYNAMICS	NEO CONSTRUCTION MATERIALS	DESIGN OF PRESTRESSED CONCRETE STRUCTURES	ENGLISH FOR RESEARCH PAPER WRITING	-----

ADDL. CONTROLLER OF EXAMINATIONS

CONTROLLER OF EXAMINATIONS

PRINCIPAL

ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE CONTROLLER OF EXAMINATIONS IMMEDIATELY