

# EXAMINATION BRANCH



## VAAGDEVI COLLEGE OF ENGINEERING

(AUTONOMOUS)

Approved by AICTE & Affiliated to JNTUH, Hyderabad

Bollikunta, Warangal Urban, Telangana - 506005



TIME TABLE FOR B.TECH IV- SEMESTER (R22) MID - II EXAMINATIONS APRIL - 2025

FORENOON: 10.00 AM TO 12.00 PM & AFTERNOON: 2.00 PM TO 4.00 PM

BRANCH	DATE/DAY				
	29.04.2025 (FN) TUESDAY	29.04.2025 (AN) TUESDAY	01.05.2025 (FN) THURSDAY	01.05.2025 (AN) THURSDAY	03.05.2025 (FN) SATURDAY
CIVIL ENGINEERING (01-CE)	CONCRETE TECHNOLOGY	STRENGTH OF MATERIALS – II	HYDRAULICS AND HYDRAULICS MACHINERY	STRUCTURAL ANALYSIS - I	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING
ELECTRICAL & ELECTRONICS ENGINEERING (02 – EEE)	SOLID MECHANICS & HYDRAULIC MACHINES	MEASUREMENTS AND INSTRUMENTATION	ELECTRICAL MACHINES-II	DIGITAL ELECTRONICS	POWER SYSTEM-II
MECHANICAL ENGINEERING (03 – ME)	KINEMATICS OF MACHINERY	FLUID MECHANICS & HYDRAULIC MACHINES	IC ENGINES & GAS TURBINES	INSTRUMENTATION AND CONTROL SYSTEMS	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING
ELECTRONICS & COMMUNICATIONS ENGINEERING (04 – ECE)	PROBABILITY THEORY AND STOCHASTIC PROCESSES	ELECTROMAGNETIC FIELDS AND TRANSMISSION LINES	ANALOG AND DIGITAL COMMUNICATIONS	LINEAR AND DIGITAL IC APPLICATIONS	ELECTRONIC CIRCUIT ANALYSIS
COMPUTER SCIENCE & ENGINEERING (05 – CSE)	DISCRETE MATHEMATICS	BUSINESS ECONOMICS & FINANCIAL ANALYSIS	OPERATING SYSTEMS	DATABASE MANAGEMENT SYSTEMS	SOFTWARE ENGINEERING
COMPUTER SCIENCE & ENGINEERING (AIML) (66 – CSM)	DISCRETE MATHEMATICS	AUTOMATA THEORY AND COMPILER DESIGN	INTRODUCTION TO ARTIFICIAL INTELLIGENCE	DATABASE MANAGEMENT SYSTEMS	OBJECT ORIENTED PROGRAMMING THROUGH JAVA
COMPUTER SCIENCE & ENGINEERING (DS) (67 – CSD)	DISCRETE MATHEMATICS	BUSINESS ECONOMICS & FINANCIAL ANALYSIS	OPERATING SYSTEMS	DATABASE MANAGEMENT SYSTEMS	SOFTWARE ENGINEERING

Additional Controller of Examinations

Controller of Examinations

Principal