

# EXAMINATION BRANCH



## VAAGDEVI COLLEGE OF ENGINEERING

(AUTONOMOUS)

Approved by AICTE & Affiliated to JNTUH, Hyderabad

Bollikunta, Warangal Urban, Telangana - 506005



TIME TABLE FOR B.TECH IV- SEMESTER (R22) MID - I EXAMINATIONS FEBRUARY - 2026

FORENOON: 10.00 AM TO 12.00 PM & AFTERNOON: 2.00 PM TO 4.00 PM

BRANCH	DATE & DAY				
	17-02-2026 (FN) TUESDAY	17-02-2026 (AN) TUESDAY	18-02-2026 (FN) WEDNESDAY	18-02-2026 (AN) WEDNESDAY	19-02-2026 (FN) THURSDAY
<b>CIVIL ENGINEERING (01-CE)</b>	CONCRETE TECHNOLOGY	STRENGTH OF MATERIALS – II	HYDRAULICS AND HYDRAULICS MACHINERY	STRUCTURAL ANALYSIS - I	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING
<b>ELECTRICAL &amp; ELECTRONICS ENGINEERING (02 – EEE)</b>	SOLID MECHANICS & HYDRAULIC MACHINES	MEASUREMENTS AND INSTRUMENTATION	ELECTRICAL MACHINES-II	DIGITAL ELECTRONICS	POWER SYSTEM-II
<b>MECHANICAL ENGINEERING (03 – ME)</b>	KINEMATICS OF MACHINERY	FLUID MECHANICS & HYDRAULIC MACHINES	IC ENGINES & GAS TURBINES	INSTRUMENTATION AND CONTROL SYSTEMS	BASIC ELECTRICAL AND ELECTRONICS ENGINEERING
<b>ELECTRONICS &amp; COMMUNICATIONS ENGINEERING (04 – ECE)</b>	PROBABILITY THEORY AND STOCHASTIC PROCESSES	ELECTROMAGNETIC FIELDS AND TRANSMISSION LINES	ANALOG AND DIGITAL COMMUNICATIONS	LINEAR AND DIGITAL IC APPLICATIONS	ELECTRONIC CIRCUIT ANALYSIS
<b>COMPUTER SCIENCE &amp; ENGINEERING (05 – CSE)</b>	DISCRETE MATHEMATICS	BUSINESS ECONOMICS & FINANCIAL ANALYSIS	OPERATING SYSTEMS	DATABASE MANAGEMENT SYSTEMS	SOFTWARE ENGINEERING
<b>COMPUTER SCIENCE &amp; ENGINEERING (AIML) (66 – CSM)</b>	DISCRETE MATHEMATICS	AUTOMATA THEORY AND COMPILER DESIGN	INTRODUCTION TO ARTIFICIAL INTELLIGENCE	DATABASE MANAGEMENT SYSTEMS	OBJECT ORIENTED PROGRAMMING THROUGH JAVA
<b>COMPUTER SCIENCE &amp; ENGINEERING (DS) (67 – CSD)</b>	DISCRETE MATHEMATICS	BUSINESS ECONOMICS & FINANCIAL ANALYSIS	OPERATING SYSTEMS	DATABASE MANAGEMENT SYSTEMS	SOFTWARE ENGINEERING

Additional Controller of Examination

Controller of Examinations

Principal